



ASYMMETRIC DUAL N-CHANNEL MOSFET

Product Summary

Device	BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C	
051/		$6m\Omega$ @ V _{GS} = 10V	11.6A	
Q1	25V	7.5mΩ @ V _{GS} = 4.5V	10.4A	
Q2	25V	2.0mΩ @ V _{GS} = 10V		20.1A
		3.1mΩ @ V _{GS} = 4.5V	16.1A	

Description

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

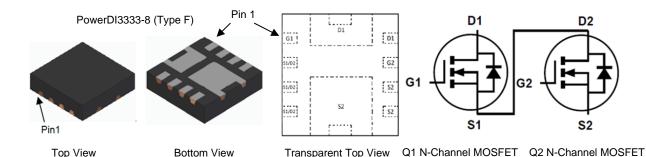
Power-management functions

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)



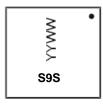
Ordering Information (Note 4)

Part Number	Paskaga	Packing		
Fait Number	Package	Qty.	Carrier	
DMT26M0LDG-7	PowerDI3333-8 (Type F)	2000	Tape & Reel	
DMT26M0LDG-13	PowerDI3333-8 (Type F)	3000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



S9S = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 24 = 2024) WW = Week Code (01 to 53)



Maximum Ratings N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Q1 N-CHANNEL	Q2 N-CHANNEL	Unit		
Drain-Source Voltage	V_{DSS}	25	25	V		
Gate-Source Voltage	Vgss	±12	±12	V		
Continuous Drain Current (Note 5) $V_{GS} = 10V$ Steady State $T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$			ID	11.6 8.4	20.1 14.5	Α
Continuous Drain Current (Note 5) V _{GS} = 4.5V	ID	10.4 33.8	16.1 52.6	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle =	IDM	77	116	Α		
Avalanche Current (Note 6) L = 1mH	las	6.5	16.5	Α		
Avalanche Energy (Note 6) L = 1mH	Eas	21	136	mJ		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	1.24	W
Thermal Resistance, Junction to Ambient (Note 5)		103	°C/W
Thermal Resistance, Junction to Case (Note 7)	Rejc	9.7	°C/W
Operating and Storage Temperature Range		-55 to +150	°C

Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
- 7. Thermal resistance from junction to soldering point (on the exposed drain pad).



Electrical Characteristics N-CHANNEL - Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	25	_	_	V	V _{GS} = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	1.0	μΑ	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	Igss	-	_	100	nA	VGS = 12V, VDS = 0V	
Gate-Source Leakage	Igss	1	_	-100	nA	$V_{GS} = -8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	0.8	_	2.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Dagger	_	3.9	6	mΩ	$V_{GS} = 10V, I_D = 13A$	
Static Drain-Source On-Resistance	RDS(ON)	-	5.0	7.5	11177	VGS = 4.5V, ID = 12A	
Diode Forward Voltage (Note 8)	VsD	_	0.7	1.0	V	Vgs = 0V, Is = 1A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	-	1010	_	pF		
Output Capacitance	Coss	_	732	_	pF	V _{DS} = 13V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	47	_	pF		
Gate Resistance	Rg	-	0.65	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	1	7.2	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	1	15.9	_	nC	\/ 12\/ I= 12A	
Gate-Source Charge	Q_{gs}	_	2.6	_	nC	V _{DS} = 13V, I _D = 13A	
Gate-Drain Charge	Q_{gd}	_	1.5	_	nC		
Turn-On Delay Time	tD(ON)	-	5.6	_	ns		
Turn-On Rise Time	tR	-	31.7	_	ns	V _{DS} = 13V	
Turn-Off Delay Time	t _{D(OFF)}		19.9	_	ns	$R_g = 6\Omega$, $I_D = 13A$	
Turn-Off Fall Time	tF	_	21.4	_	ns		

Electrical Characteristics N-CHANNEL - Q2 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	25	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	-	100	nA	V _G S = 12V, V _D S = 0V	
Gate-Source Leakage	Igss	_	_	-100	nA	$V_{GS} = -8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(th)	1.1	-	2.2	V	V _{DS} = V _{GS} , I _D = 1mA	
Static Drain-Source On-Resistance	Descent	_	1.1	2.0	mΩ	V _G S = 10V, I _D = 27A	
Static Dialii-Source Off-Resistance	RDS(ON)	_	1.5	3.1	11122	$V_{GS} = 4.5V, I_D = 24A$	
Diode Forward Voltage (Note 8)	VsD	_	0.7	1.0	V	V _G S = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	4016	_	pF		
Output Capacitance	Coss	1	2624	ı	pF	$V_{DS} = 13V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	1	135	ı	pF]	
Gate Resistance	Rg	1	0.49	ı	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	26.7	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	57.4	_	nC	V 40V I- 07A	
Gate-Source Charge	Qgs	_	8.6	_	nC	V _{DS} = 13V, I _D = 27A	
Gate-Drain Charge	Qgd	_	6.9	_	nC	1	
Turn-On Delay Time	tD(ON)	_	12.4	_	ns		
Turn-On Rise Time	t _R	_	37.2	_	ns	V _{DS} =13V	
Turn-Off Delay Time	tD(OFF)	_	62.7	_	ns	$R_G = 6\Omega$, $I_D = 27A$	
Turn-Off Fall Time	tF	_	30.8	_	ns	1	

Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.



N-CHANNEL - Q1

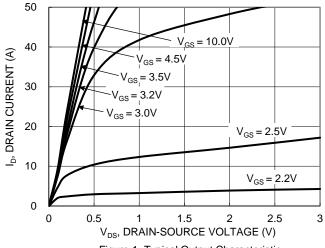
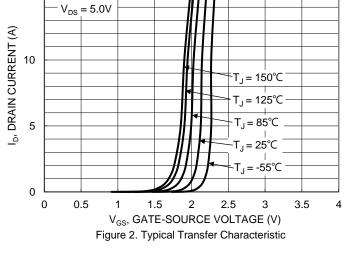


Figure 1. Typical Output Characteristic



15

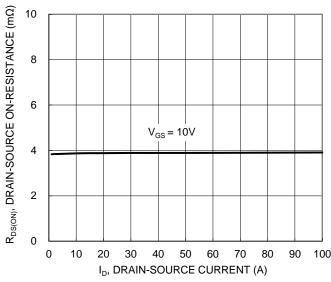


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

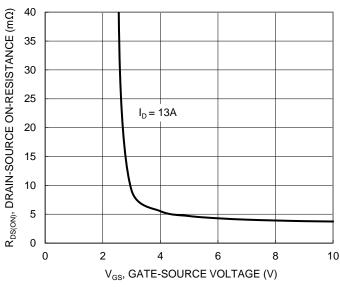


Figure 4. Typical Transfer Characteristic

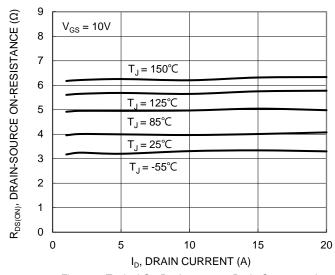


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

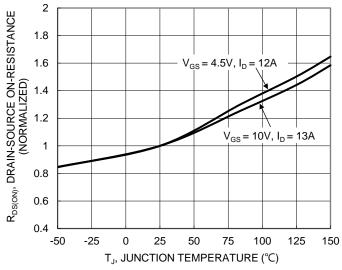


Figure 6. On-Resistance Variation with Temperature





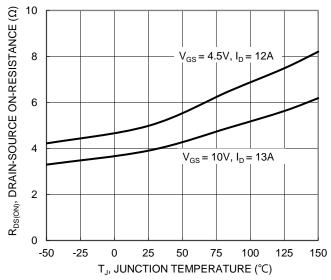
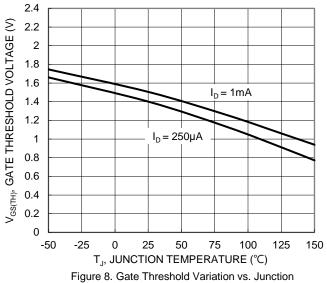


Figure 7. On-Resistance Variation with Temperature



Temperature

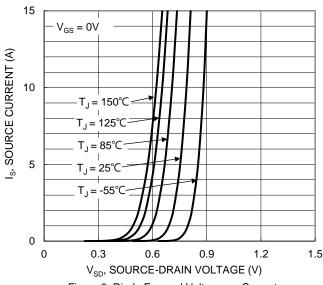
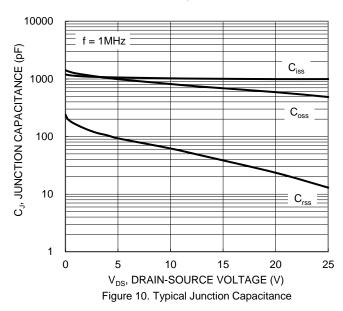


Figure 9. Diode Forward Voltage vs. Current



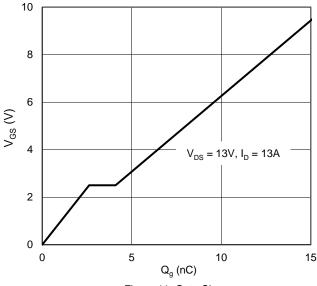


Figure 11. Gate Charge

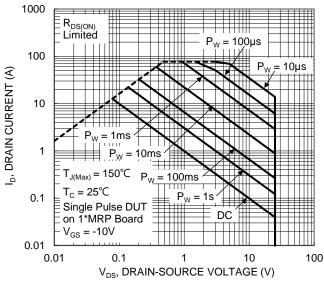
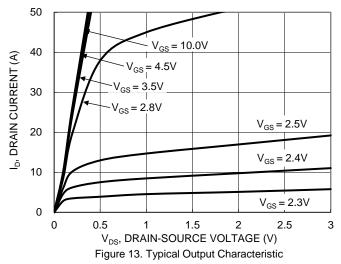
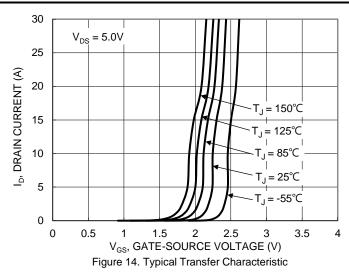


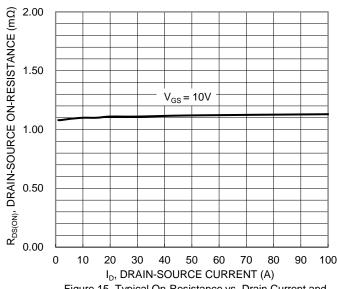
Figure 12. SOA, Safe Operation Area



N-CHANNEL - Q2







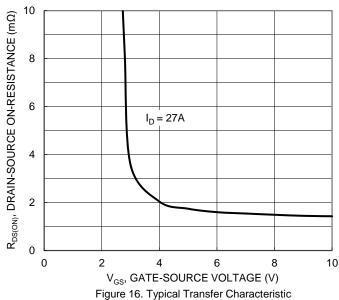
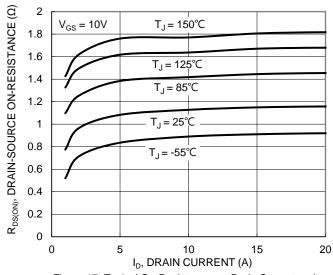


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage



R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE 1.8 $V_{GS} = 10V, I_D = 27A$ 1.6 (NORMALIZED) 1.4 1.2 $V_{GS} = 4.5V, I_D = 24A$ 1 8.0 0.6 0.4 -50 -25 0 25 50 75 100 125 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 17. Typical On-Resistance vs. Drain Current and Temperature

Figure 18. On-Resistance Variation with Temperature

2



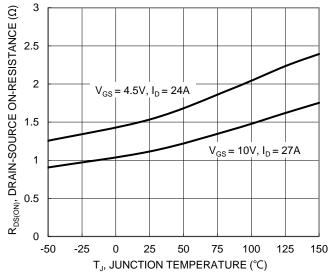
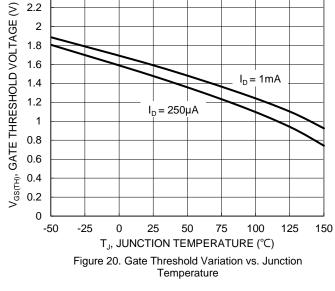


Figure 19. On-Resistance Variation with Temperature



2.4

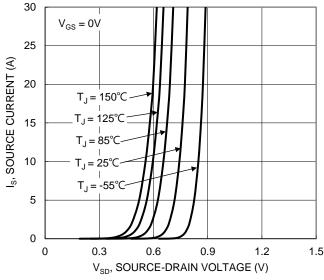
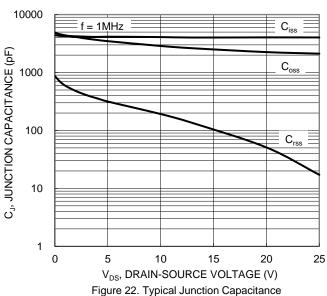


Figure 21. Diode Forward Voltage vs. Current



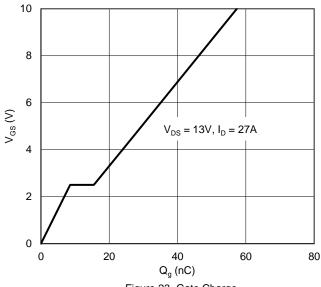
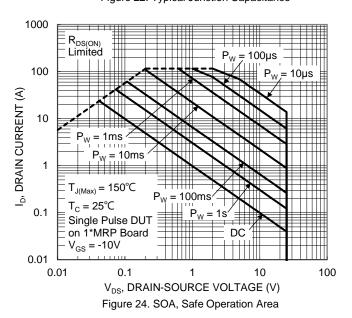


Figure 23. Gate Charge



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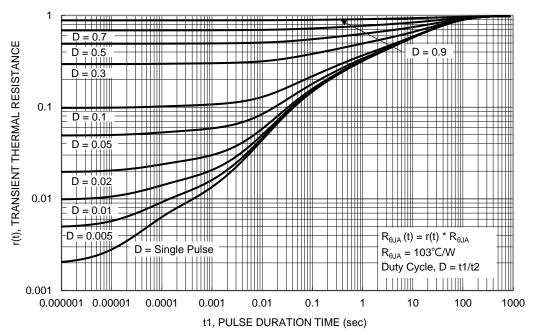


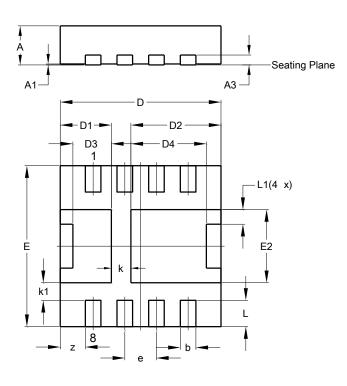
Figure 25. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type F)

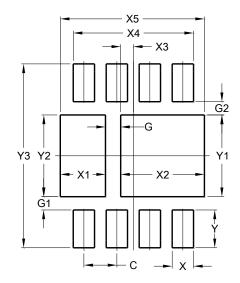


PowerDI3333-8 (Type F)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A 1	0.00	0.05	0.02			
A3		-	0.203			
b	0.27	0.37	0.32			
D	3.25	3.35	3.30			
D1	0.95	1.15	1.05			
D2	1.75	1.95	1.85			
D3	0.69	0.89	0.79			
D4	1.45	1.65	1.55			
Е	3.25	3.35	3.30			
E2	1.40	1.60	1.50			
е	0.65BSC					
L	0.49	0.59	0.54			
L1	0.20	0.40	0.30			
Z			0.515			
k			0.40			
k1			0.36			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type F)



Dimensions	Value		
Dimonorono	(in mm)		
С	0.650		
G	0.300		
G1	0.260		
G2	0.260		
Х	0.420		
X1	0.890		
X2	1.650		
Х3	0.250		
X4	2.370		
X5	2.840		
Υ	0.740		
Y1	1.600		
Y2	1.600		
Y3	3.600		



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