



100V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D Max T _C = +25°C | |
|-------------------|-------------------------------|--|--|
| 1001/ | 30mΩ @ V _{GS} = 10V | 26A | |
| 100V | 50mΩ @ V _{GS} = 4.5V | 21A | |

Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH10H032LFVWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description

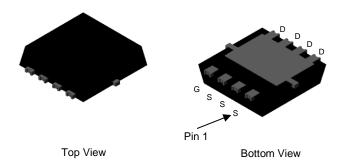
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

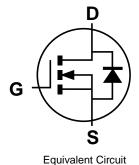
- Backlighting
- Power-management functions
- DC-DC converters

Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.03 grams (Approximate)

PowerDI3333-8/SWP (Type UX)





Ordering Information (Note 4)

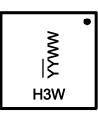
| Part Number | Package | Packing | | |
|--------------------|-----------------------------|---------|---------------|--|
| Fait Number | rackage | Qty. | Carrier | |
| DMTH10H032LFVWQ-7 | PowerDI3333-8/SWP (Type UX) | 2,000 | Tape and Reel | |
| DMTH10H032LFVWQ-13 | PowerDI3333-8/SWP (Type UX) | 3,000 | Tape and Reel | |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



H3W = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 24 = 2024) WW = Week Code (01 to 53)

| Characteristic | Symbol | Value | Unit | |
|---|------------------------|-------|------|---|
| Drain-Source Voltage | VDSS | 100 | V | |
| Gate-Source Voltage | Vgss | ±20 | V | |
| Continuous Drain Current (Note 5) Vac. 10V | T _C = +25°C | - | 26 | A |
| Continuous Drain Current (Note 5) V _{GS} = 10V | $T_C = +100$ °C | ID | 18 | |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | lом | 104 | А | |
| Maximum Continuous Body Diode Forward Current (Note 5) | Is | 26 | Α | |
| Pulsed Body Diode Forward Current (Note 6) | Ism | 104 | А | |
| Avalanche Current, L = 0.3mH (Note 6) | las | 13 | А | |
| Avalanche Energy, L = 0.3mH (Note 6) | E _{AS} | 25.3 | mJ | |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | |
|---|----------------|----------|-------------|-------|
| Total Power Dissipation (Note 7) | PD | 1.7 | W | |
| Thermal Resistance, Junction to Ambient (Note 7) | Steady State | RθJA | 90 | °C/W |
| Total Power Dissipation (Note 8) | P _D | 3.8 | W | |
| Thermal Resistance, Junction to Ambient (Note 8) Steady State | | Reja | 40 | 90044 |
| Thermal Resistance, Junction to Case (Note 5) | Rejc | 3.3 | °C/W | |
| Operating and Storage Temperature Range | | TJ, TSTG | -55 to +175 | °C |

Notes:

- 5. Thermal resistance from junction to soldering point (on the exposed drain pad).

- 6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.

 7. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

 8. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

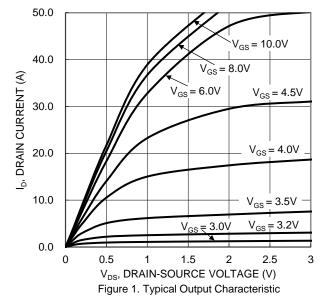
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|--|-----------------------------------|-----|------|------|------------|--|--|
| OFF CHARACTERISTICS (Note 9) | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 100 | _ | _ | V | $V_{GS} = 0V, I_{D} = 1mA$ | |
| Zero Gate Voltage Drain Current | IDSS | _ | _ | 1 | μΑ | V _{DS} = 80V, V _{GS} = 0V | |
| Gate-Source Leakage | Igss | _ | _ | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 9) | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 1.3 | | 2.5 | V | $V_{DS} = V_{GS}$, $I_D = 250\mu A$ | |
| Static Drain-Source On-Resistance | D | _ | 22 | 30 | ~ 0 | $V_{GS} = 10V, I_D = 10A$ | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | _ | 32 | 50 | mΩ | $V_{GS} = 4.5V, I_D = 5A$ | |
| Diode Forward Voltage | VsD | _ | 0.8 | 1 | V | V _G S = 0V, I _S = 6A | |
| DYNAMIC CHARACTERISTICS (Note 10) | DYNAMIC CHARACTERISTICS (Note 10) | | | | | | |
| Input Capacitance | Ciss | _ | 683 | _ | pF | V _{DS} = 50V, V _{GS} = 0V f = 1MHz | |
| Output Capacitance | Coss | 1 | 165 | | pF | | |
| Reverse Transfer Capacitance | Crss | | 6.9 | _ | pF | 11 = 11VIDZ | |
| Gate Resistance | Rg | _ | 1.2 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$ | |
| Total Gate Charge (V _{GS} = 4.5V) | Qg | 1 | 6.3 | _ | nC | | |
| Total Gate Charge (V _{GS} = 10V) | Qg | ı | 11.9 | ı | nC | V _{DS} = 50V, I _D = 6A | |
| Gate-Source Charge | Qgs | ı | 2.0 | ı | nC | | |
| Gate-Drain Charge | Q_{gd} | | 3.1 | _ | nC | | |
| Turn-On Delay Time | td(ON) | _ | 4.1 | _ | ns | | |
| Turn-On Rise Time | t _R | _ | 4.5 | _ | ns | $V_{DS} = 50V, R_L = 5.85\Omega$ $V_{GS} = 10V, R_g = 3\Omega$ | |
| Turn-Off Delay Time | tD(OFF) | _ | 12.5 | _ | ns | | |
| Turn-Off Fall Time | t _F | _ | 9.3 | _ | ns | | |
| Reverse Recovery Time | trr | _ | 31.5 | _ | ns | $I_{\rm F} = 6A di/dt = 500A/us$ | |
| Reverse Recovery Charge | Qrr | | 94.6 | _ | nC | | |

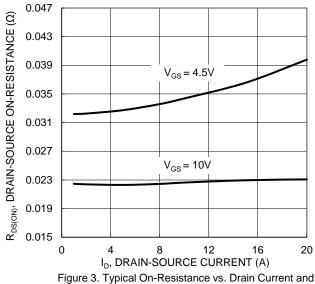
Notes:

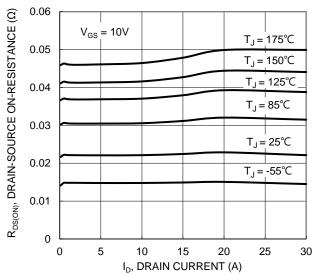
Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





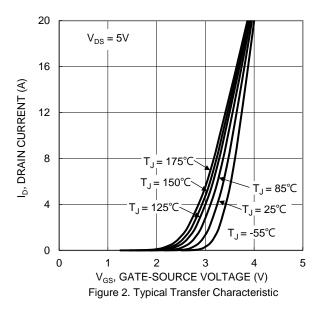


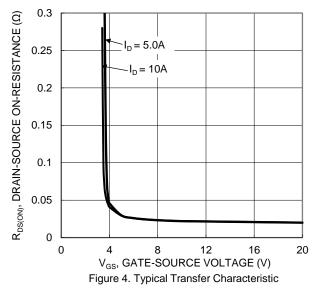




Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature





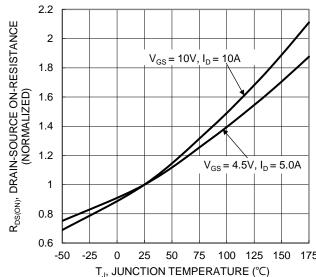


Figure 6. On-Resistance Variation with Temperature





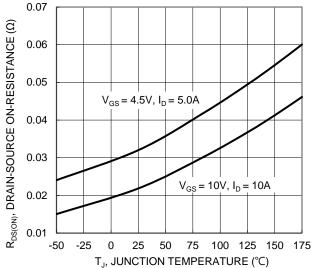


Figure 7. On-Resistance Variation with Temperature

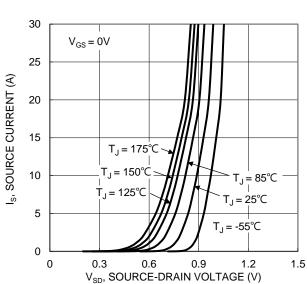
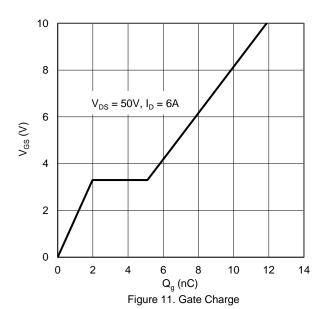
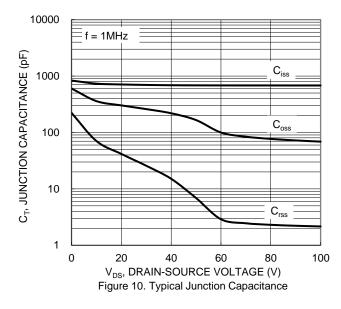


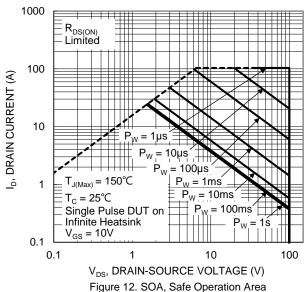
Figure 9. Diode Forward Voltage vs. Current



3 $V_{GS(TH)}$, GATE THRESHOLD VOLTAGE (V) 2.5 $I_D = 1mA$ 2 1.5 $I_D = 250 \mu A$ 1 0.5 0 -25 25 50 75 100 125 150 175 -50 0 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature





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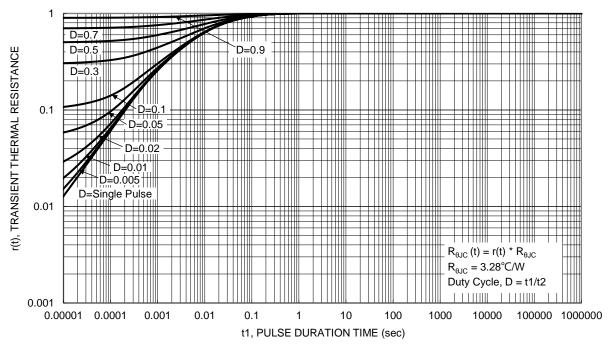


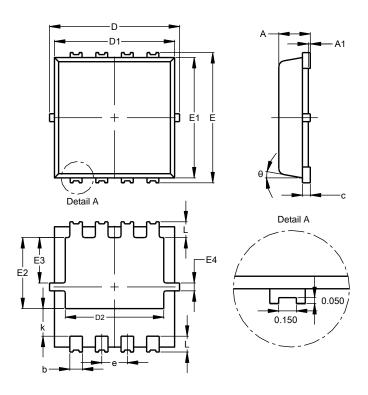
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8/SWP (Type UX)

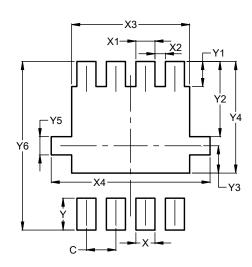


| PowerDI3333-8/SWP | | | | | |
|----------------------|------|------|------|--|--|
| (Type UX) | | | | | |
| Dim | Min | Max | Тур | | |
| Α | 0.75 | 0.85 | 0.80 | | |
| A1 | 0.00 | 0.05 | | | |
| b | 0.25 | 0.40 | 0.32 | | |
| С | 0.10 | 0.25 | 0.15 | | |
| D | 3.20 | 3.40 | 3.30 | | |
| D1 | 2.95 | 3.15 | 3.05 | | |
| D2 | 2.30 | 2.70 | 2.50 | | |
| Е | 3.20 | 3.40 | 3.30 | | |
| E1 | 2.95 | 3.15 | 3.05 | | |
| E2 | 1.60 | 2.00 | 1.80 | | |
| E3 | 0.95 | 1.35 | 1.15 | | |
| E4 | 0.10 | 0.30 | 0.20 | | |
| е | _ | _ | 0.65 | | |
| k | 0.50 | 0.90 | 0.70 | | |
| L | 0.30 | 0.50 | 0.40 | | |
| θ | 0° | 12° | 10° | | |
| All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8/SWP (Type UX)



| Dimensions | Value (in mm) | | | |
|------------|---------------|--|--|--|
| C | 0.650 | | | |
| X | 0.420 | | | |
| X1 | 0.420 | | | |
| X2 | 0.230 | | | |
| Х3 | 2.600 | | | |
| X4 | 3.500 | | | |
| Υ | 0.700 | | | |
| Y1 | 0.550 | | | |
| Y2 | 1.650 | | | |
| Y3 | 0.600 | | | |
| Y4 | 2.450 | | | |
| Y5 | 0.400 | | | |
| Y6 | 3.700 | | | |



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