

40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BVDSS	R _{DS(ON)} Max	I _D Max (Note 9) T _C = +25°C	
	3.0mΩ @ V _{GS} = 10V	100A	
40V	5.0mΩ @ V _{GS} = 5V	78A	

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

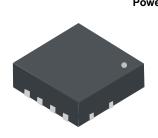
- Motor controls
- DC-DC converters
- Power management

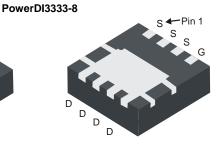
Features and Benefits

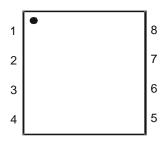
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching, Test in Production Ensures More Reliable and Robust End Application
- Low Rds(ON) Ensures On-State Losses are Minimized
- Excellent Q_{GD} × R_{DS(ON)} Product (FOM)
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

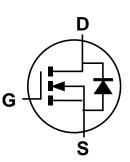
Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202. Method 208 @3
- Weight: 0.034 grams (Approximate)









Top View

Bottom View

Top View

Equivalent Circuit

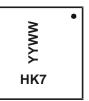
Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Nullibei	rackaye	Qty.	Carrier	
DMTH43M7LFG-7	PowerDI3333-8	2,000	Tape & Reel	
DMTH43M7LFG-13	PowerDI3333-8	3,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



HK7 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 23 = 2023) WW = Week Code (01 to 53)



Maximum Ratings (@ $T_C = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	40	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current (Note 6) (Note 9) $V_{GS} = 10V$ $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$		I _D	100 85	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Ірм	400	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	100	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle :	Ism	400	Α	
Avalanche Current, L = 1mH	las	19.3	Α	
Avalanche Energy, L = 1mH	E _{AS}	185.3	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.5	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	43	°C/W	
Total Power Dissipation (Note 6)	PD	65.2	W	
Thermal Resistance, Junction to Case (Note 6)		Rejc	2.3	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@TJ = +25°C, unless otherwise specified.)

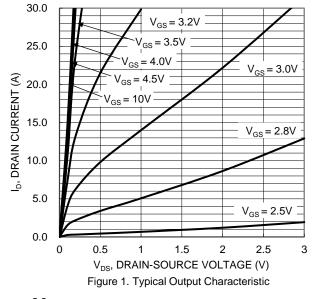
Characteristic		Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	-	1	μΑ	$V_{DS} = 32V$, $V_{GS} = 0V$	
Gate-Source Leakage		_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	1	-	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Dragovi	_	1.9	3.0	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Dialit-Source Off-Nesistance	RDS(ON)	_	2.5	5.0		V _G S = 5V, I _D = 15A	
Diode Forward Voltage	VsD	_	0.8	1.0	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	2182	_	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	1395	_			
Reverse Transfer Capacitance	Crss	_	59	_			
Gate Resistance	Rg	_	1.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	30	_		.,	
Gate-Source Charge	Q _{GS}	_	7	_	nC	$V_{DS} = 20V, I_{D} = 20A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q _{GD}	_	3.6	_		VGS = 10V	
Turn-On Delay Time	td(on)	_	5.7	_		$V_{DD} = 20V, V_{GS} = 10V,$ $R_G = 1.6\Omega, I_D = 20A$	
Turn-On Rise Time	t _R	_	5.4	_			
Turn-Off Delay Time	tD(OFF)	_	22.4	_	ns		
Turn-Off Fall Time	tF	_	11.1	_			
Body Diode Reverse Recovery Time	trr		39.5	_	ns	I_ 15A di/dt 100A/vo	
Body Diode Reverse Recovery Charge	QrR	_	39.7	_	nC	I _F = 15A, di/dt = 100A/μs	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.
 9. Package limit.







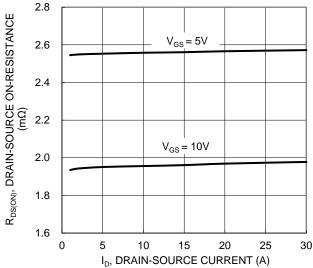


Figure 3. Typical On-Resistance vs. Drain Current

and Gate Voltage

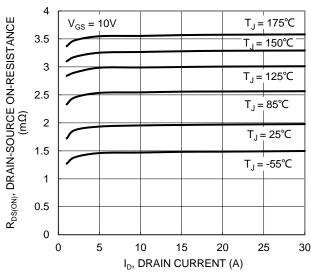
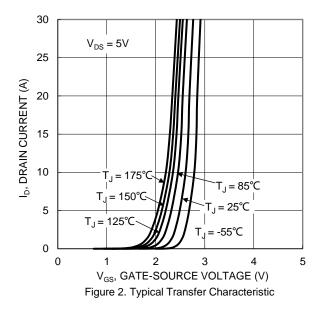
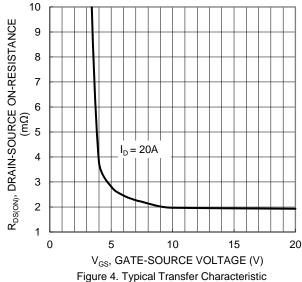


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





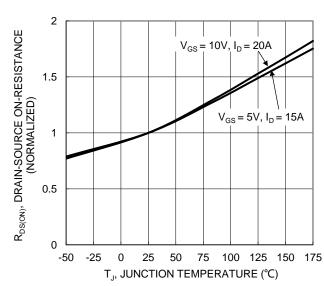


Figure 6. On-Resistance Variation with Temperature





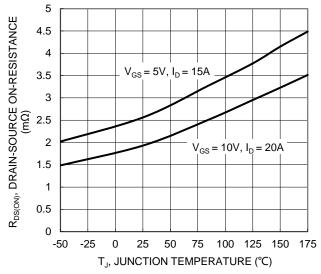
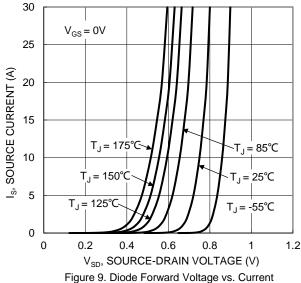


Figure 7. On-Resistance Variation with Temperature



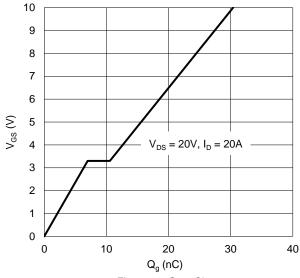


Figure 11. Gate Charge

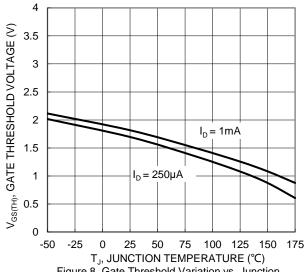


Figure 8. Gate Threshold Variation vs. Junction Temperature

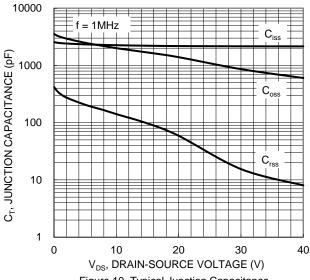


Figure 10. Typical Junction Capacitance

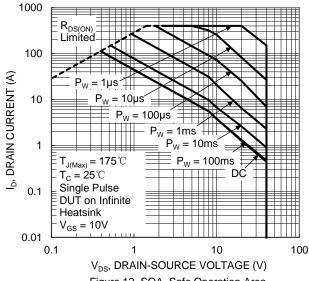


Figure 12. SOA, Safe Operation Area



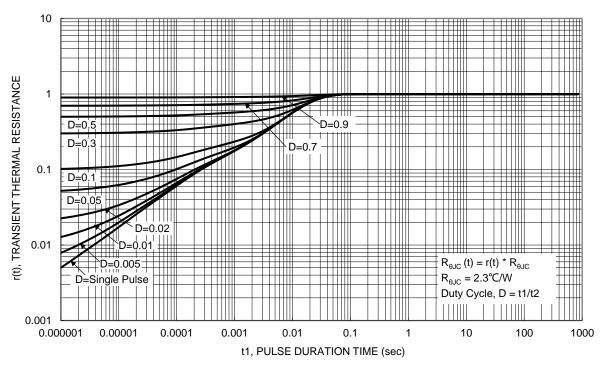


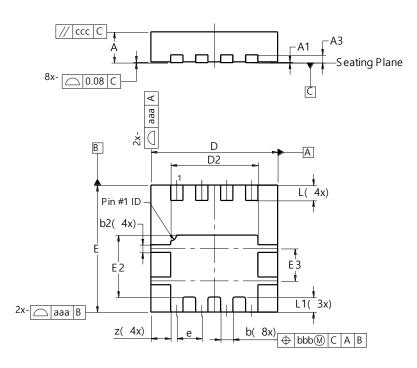
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

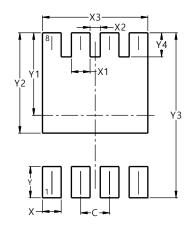


PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	1	-	0.203			
b	0.27	0.37	0.32			
b2	-	-	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
е	_	_	0.65			
L	0.35	0.45	0.40			
L1	_	-	0.39			
z	_	_	0.515			
aaa	0.25					
bbb	0.10					
CCC	0.10					
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Χ	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



IMPORTANT NOTICE

- 1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- 2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- 3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
- 4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- provided Diodes' products are subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- 6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- 7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- 8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-and-conditions/important-notice

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. All other trademarks are the property of their respective owners. © 2023 Diodes Incorporated. All Rights Reserved.

www.diodes.com