

Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D T _C = +25°C (Note 9) |
|-------------------|-------------------------------|--|
| 40V | 3.3mΩ @ V _{GS} = 10V | 100A |
| | 5.0mΩ @ V _{GS} = 5V | 95A |

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

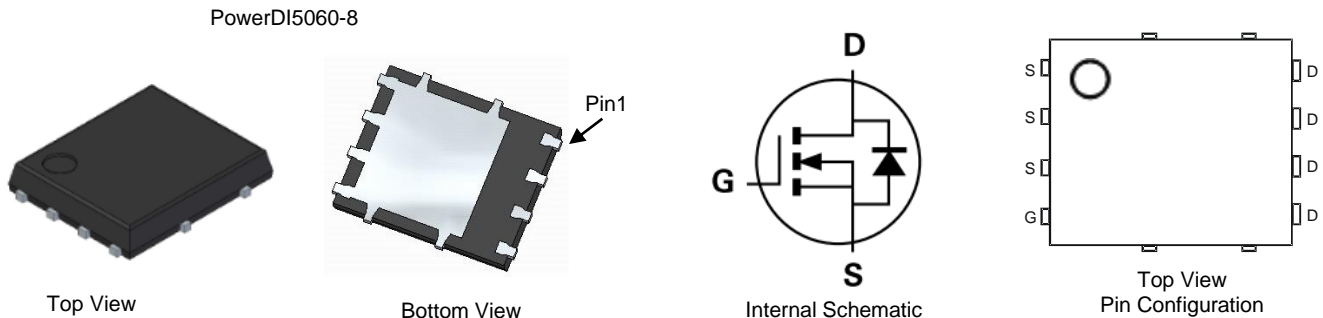
- BLDC motors
- DC-DC converters
- Load switches

Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable And Robust End Application
- Low R_{DS(ON)} – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DMTH43M8LPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**
<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (Approximate)

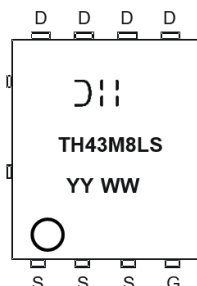


Ordering Information (Note 4)

| Part Number | Package | Packing | |
|-----------------|---------------|---------|-------------|
| | | Qty. | Carrier |
| DMTH43M8LPSQ-13 | PowerDI5060-8 | 2,500 | Tape & Reel |

- Note:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



= Manufacturer's Marking
 TH43M8LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year Code (ex: 24 = 2024)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---|------------------|-------------------------|------|
| Drain-Source Voltage | V _{DSS} | 40 | V |
| Gate-Source Voltage | V _{GSS} | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 5) | I _D | T _A = +25°C | 22 |
| | | T _A = +100°C | 15.5 |
| Continuous Drain Current, V _{GS} = 10V (Note 6) (Note 9) | I _D | T _C = +25°C | 100 |
| | | T _C = +100°C | 82 |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | I _{DM} | 350 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) | I _S | 69 | A |
| Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%) | I _{SM} | 350 | A |
| Avalanche Current, L = 1mH | I _{AS} | 13.2 | A |
| Avalanche Energy, L = 1mH | E _{AS} | 87 | mJ |

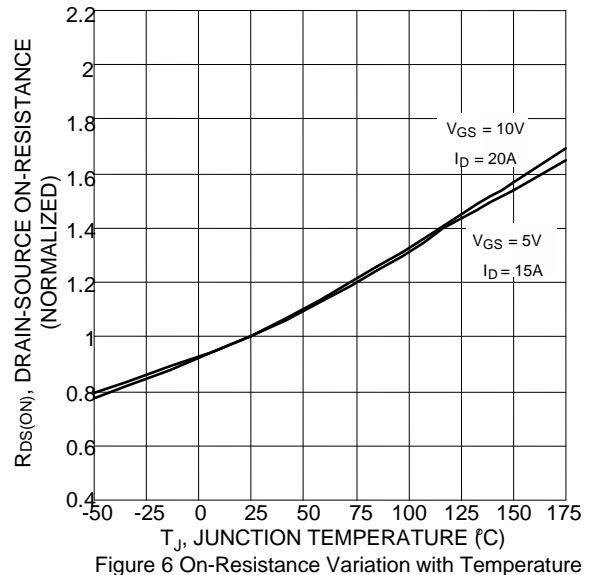
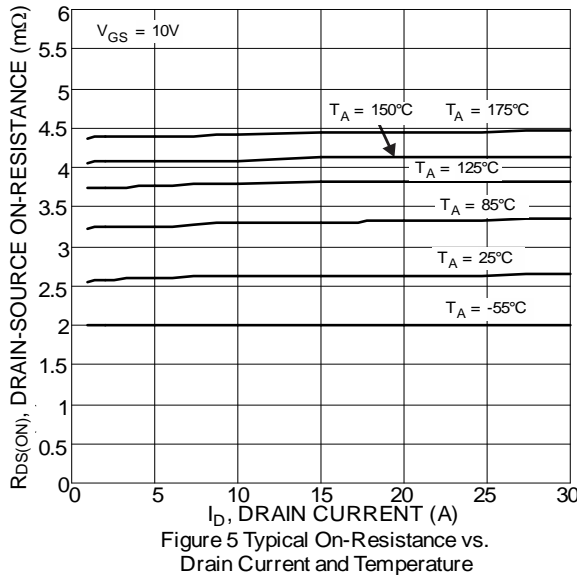
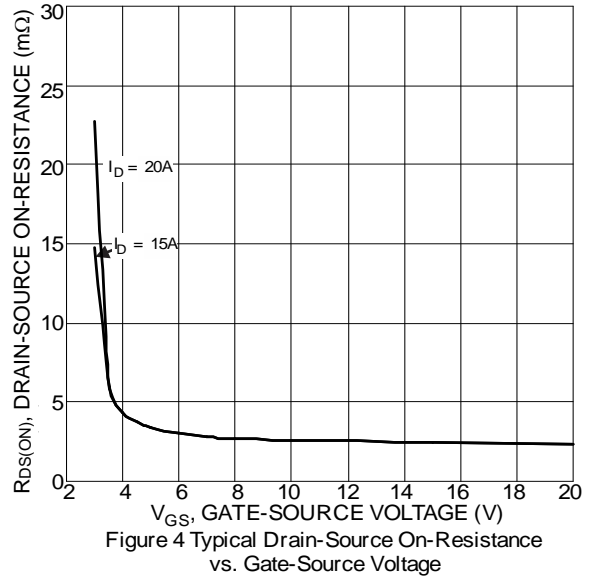
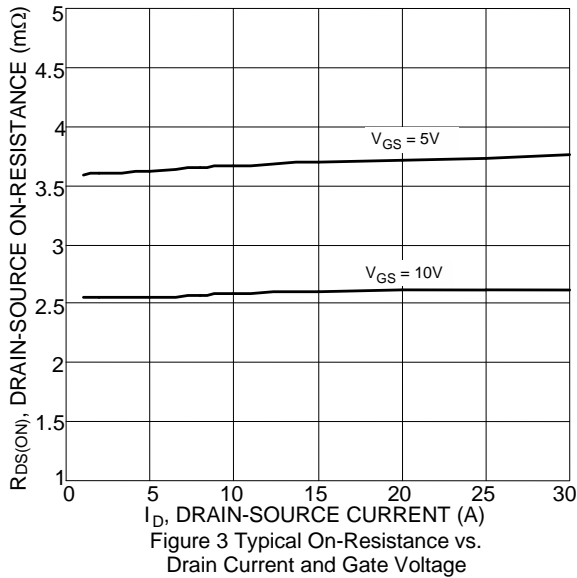
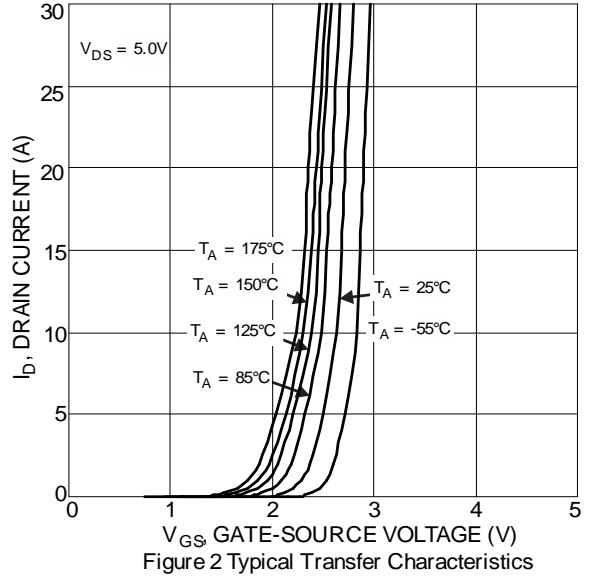
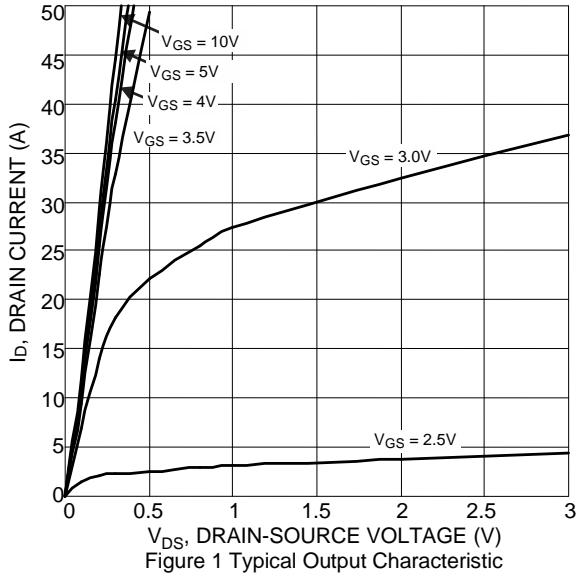
Thermal Characteristics

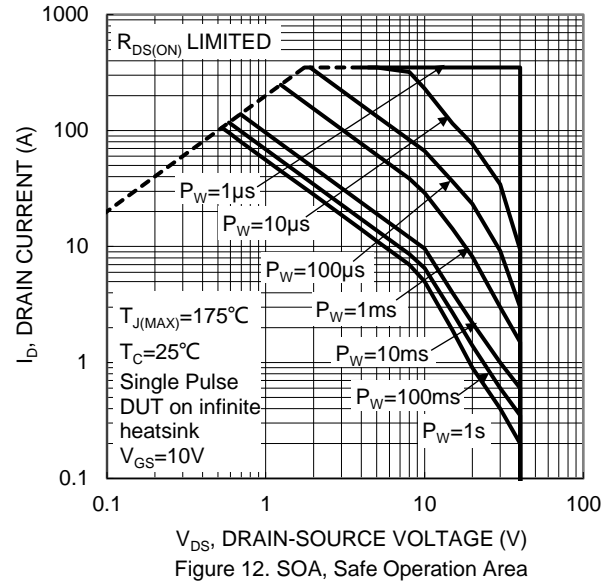
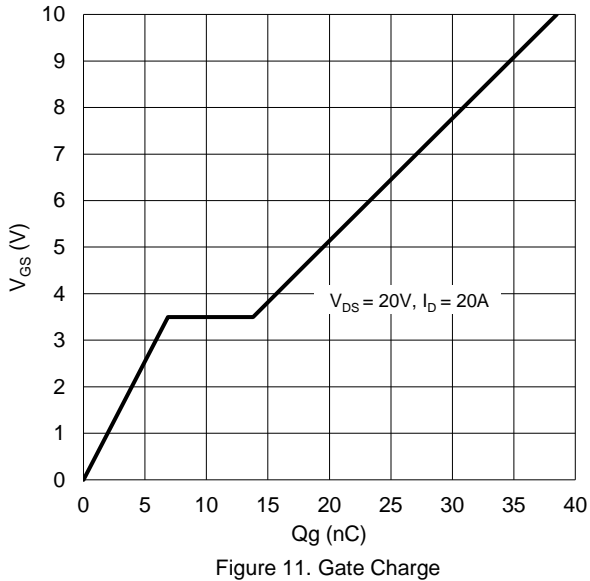
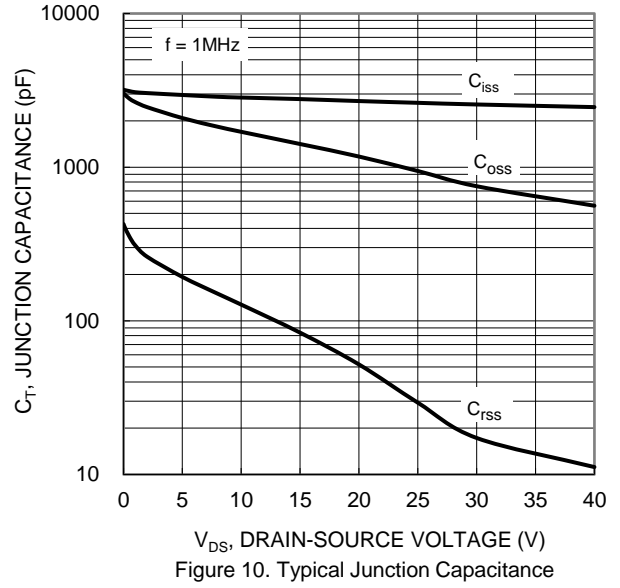
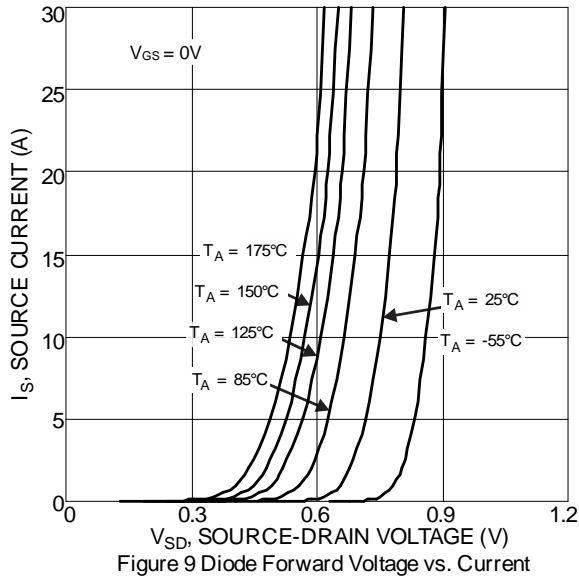
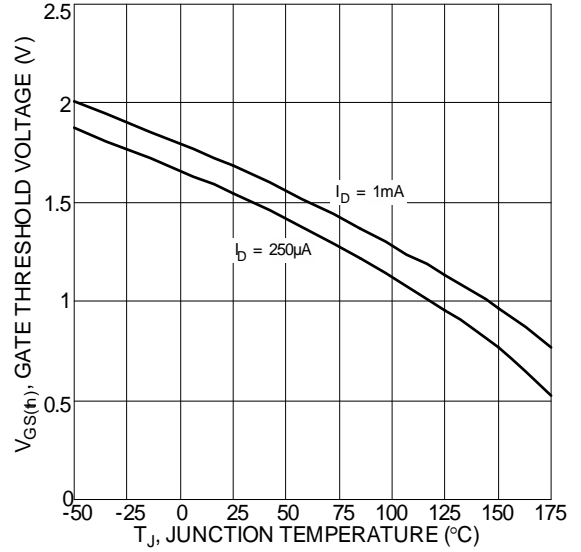
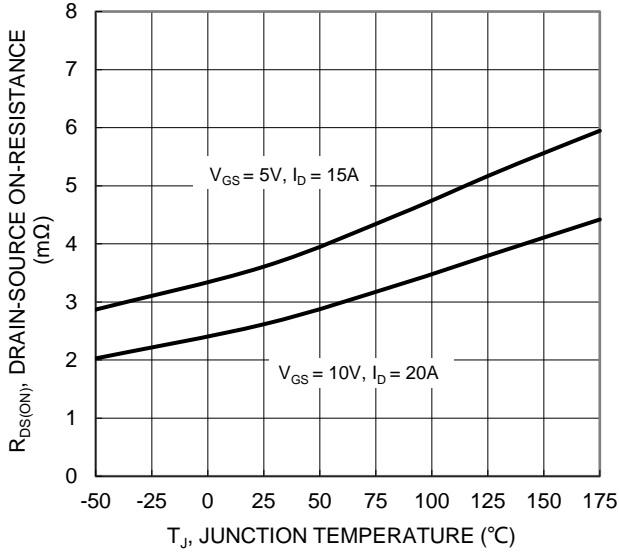
| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Total Power Dissipation (Note 5) | P _D | 2.7 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | R _{θJA} | 55 | °C/W |
| Total Power Dissipation (Note 6) | P _D | 83 | W |
| Thermal Resistance, Junction to Case (Note 6) | R _{θJC} | 1.8 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +175 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|---------------------|-----|-------|-------|------|---|
| OFF CHARACTERISTICS (Note 7) | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 40 | — | — | V | V _{GS} = 0V, I _D = 1mA |
| Zero Gate Voltage Drain Current | I _{DSS} | — | — | 1 | µA | V _{DS} = 32V, V _{GS} = 0V |
| Gate-Source Leakage | I _{GSS} | — | — | ±100 | nA | V _{GS} = ±20V, V _{DS} = 0V |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 1 | — | 2.5 | V | V _{DS} = V _{GS} , I _D = 250µA |
| Static Drain-Source On-Resistance (T _C = +25°C) | R _{DS(ON)} | — | 2.7 | 3.3 | mΩ | V _{GS} = 10V, I _D = 20A |
| | | — | 3.6 | 5.0 | | V _{GS} = 5V, I _D = 15A |
| Static Drain-Source On-Resistance (T _C = +175°C) (Note 8) | R _{DS(ON)} | — | 4.7 | — | mΩ | V _{GS} = 10V, I _D = 20A |
| Diode Forward Voltage | V _{SD} | — | — | 1.2 | V | V _{GS} = 0V, I _S = 20A |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | |
| Input Capacitance | C _{ISS} | — | 2,693 | 3,367 | pF | V _{DS} = 20V, V _{GS} = 0V, f = 1MHz |
| Output Capacitance | C _{OSS} | — | 850 | 1105 | | |
| Reverse Transfer Capacitance | C _{RSS} | — | 52 | 104 | | |
| Gate Resistance | R _G | — | 2.54 | 5.1 | Ω | V _{DS} = 0V, V _{GS} = 0V, f = 1MHz |
| Total Gate Charge (V _{GS} = 10V) | Q _G | — | 38.5 | 49 | nC | V _{DS} = 20V, I _D = 20A |
| Total Gate Charge (V _{GS} = 4.5V) | Q _G | — | 17.6 | 22 | | |
| Gate-Source Charge | Q _{GS} | — | 6.9 | 11 | | |
| Gate-Drain Charge | Q _{GD} | — | 6.9 | 11 | | |
| Turn-On Delay Time | t _{D(ON)} | — | 5.2 | 10 | ns | V _{DD} = 20V, V _{GS} = 10V, I _D = 20A, R _G = 1.6Ω |
| Turn-On Rise Time | t _R | — | 5.7 | 11 | | |
| Turn-Off Delay Time | t _{D(OFF)} | — | 23.5 | 46 | | |
| Turn-Off Fall Time | t _F | — | 11 | 22 | | |
| Body Diode Reverse Recovery Time | t _R R | — | 35.4 | 70 | ns | I _F = 15A, di/dt = 100A/µs |
| Body Diode Reverse Recovery Charge | Q _R R | — | 32.9 | — | nC | |

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.
 - Package limit.





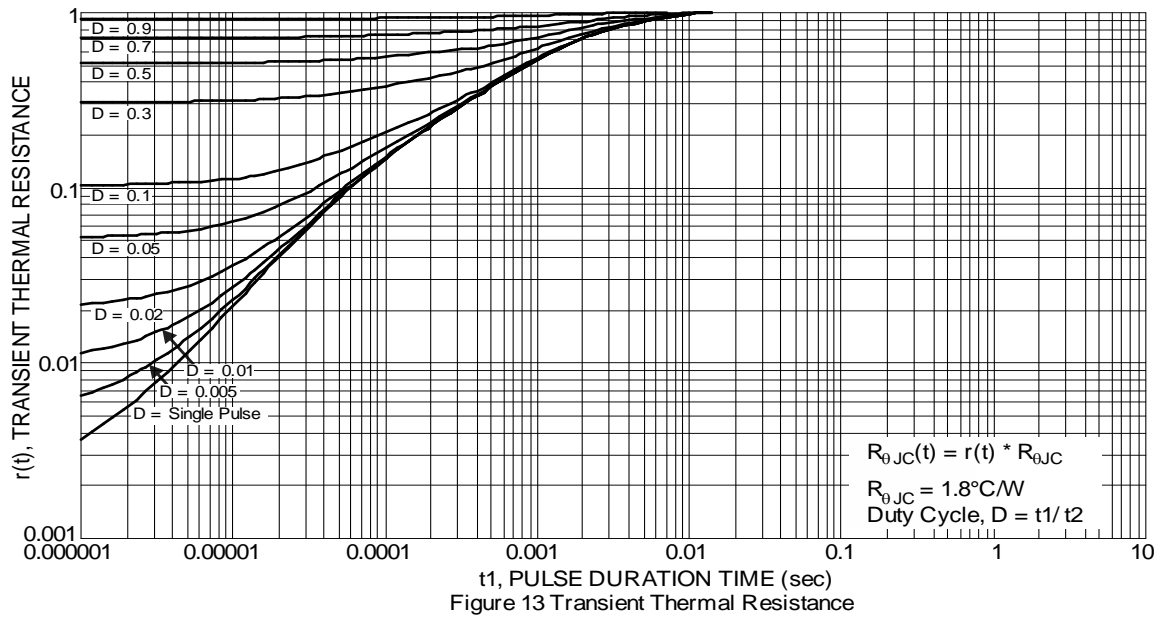
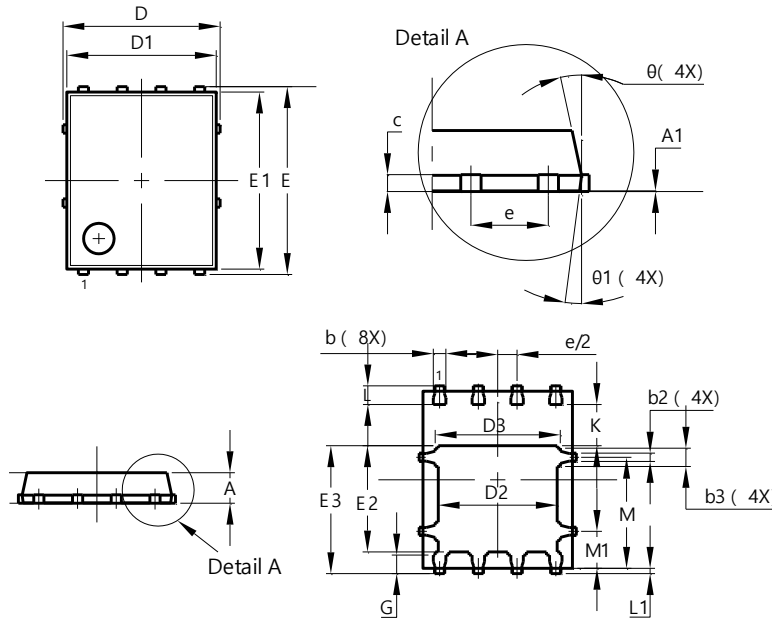


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8

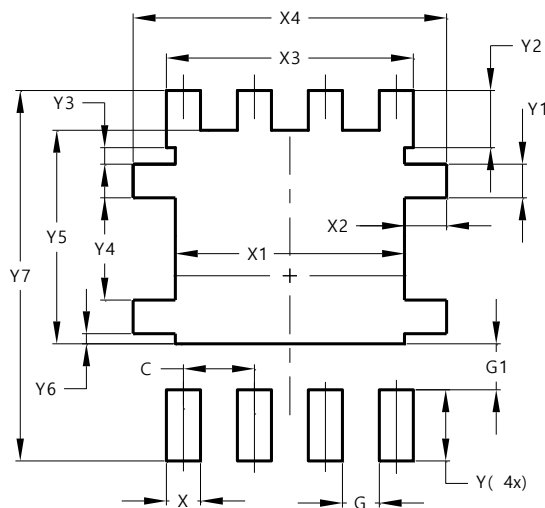


| PowerDI5060-8 | | | |
|----------------------|----------|-------|-------|
| Dim | Min | Max | Typ |
| A | 0.90 | 1.10 | 1.00 |
| A1 | 0.00 | 0.05 | - |
| b | 0.33 | 0.51 | 0.41 |
| b2 | 0.200 | 0.350 | 0.273 |
| b3 | 0.40 | 0.80 | 0.60 |
| c | 0.230 | 0.330 | 0.277 |
| D | 5.15 BSC | | |
| D1 | 4.70 | 5.10 | 4.90 |
| D2 | 3.70 | 4.10 | 3.90 |
| D3 | 3.90 | 4.30 | 4.10 |
| E | 6.15 BSC | | |
| E1 | 5.60 | 6.00 | 5.80 |
| E2 | 3.28 | 3.68 | 3.48 |
| E3 | 3.99 | 4.39 | 4.19 |
| e | 1.27 BSC | | |
| G | 0.51 | 0.71 | 0.61 |
| K | 0.51 | - | - |
| L | 0.51 | 0.71 | 0.61 |
| L1 | 0.100 | 0.200 | 0.175 |
| M | 3.235 | 4.035 | 3.635 |
| M1 | 1.00 | 1.40 | 1.21 |
| θ | 10° | 12° | 11° |
| θ_1 | 6° | 8° | 7° |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 1.270 |
| G | 0.660 |
| G1 | 0.820 |
| X | 0.610 |
| X1 | 4.100 |
| X2 | 0.755 |
| X3 | 4.420 |
| X4 | 5.610 |
| Y | 1.270 |
| Y1 | 0.600 |
| Y2 | 1.020 |
| Y3 | 0.295 |
| Y4 | 1.825 |
| Y5 | 3.810 |
| Y6 | 0.180 |
| Y7 | 6.610 |

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