

Product Summary

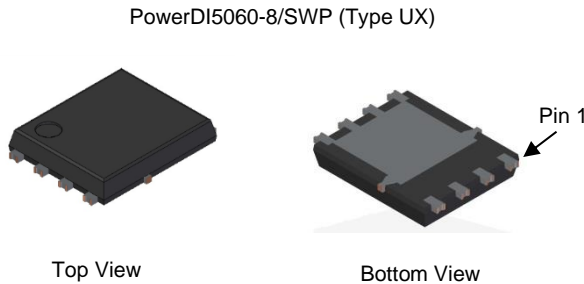
BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C (Note 7)
40V	3.3mΩ @ V _{GS} = 10V	100A
	5.0mΩ @ V _{GS} = 5V	95A

Description

This new generation N-channel enhancement mode MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance.

Applications

- BLDC motors
- DC-DC converters
- Load switches

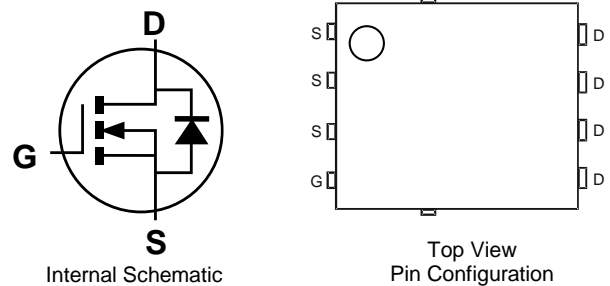


Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable And Robust End Application
- Low R_{DS(ON)} – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**
<https://www.diodes.com/quality/product-definitions/>
- **An automotive-compliant part is available under separate datasheet (DMTH43M8LPSWQ)**

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

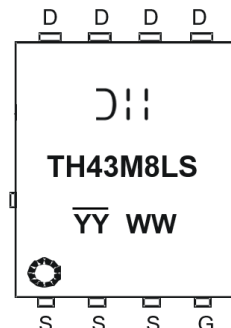


Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMTH43M8LPSW-13	PowerDI5060-8/SWP (Type UX)	2500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
 7. Package limit.

Marking Information



DII = Manufacturer's Marking
 TH43M8LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 24 = 2024)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	I _D	T _A = +25°C	22
		T _A = +100°C	15.5
Continuous Drain Current, V _{GS} = 10V (Notes 6 & 7)	I _D	T _C = +25°C	100
		T _C = +100°C	82
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	350	A
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	69	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I _{SM}	350	A
Avalanche Current, L = 1mH	I _{AS}	13.2	A
Avalanche Energy, L = 1mH	E _{AS}	87	mJ

Thermal Characteristics

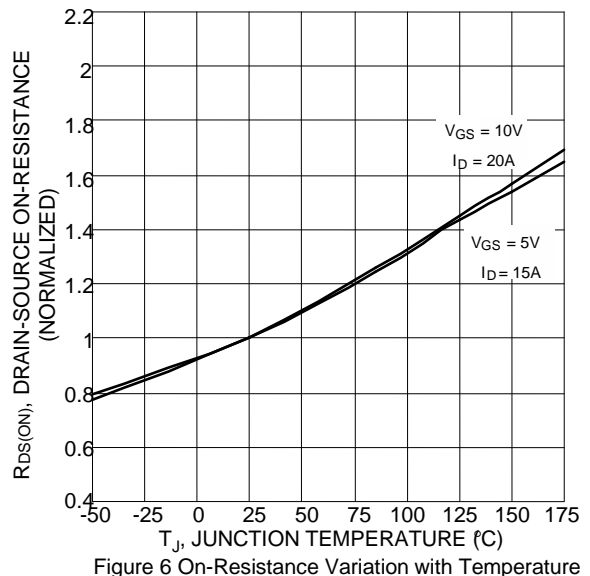
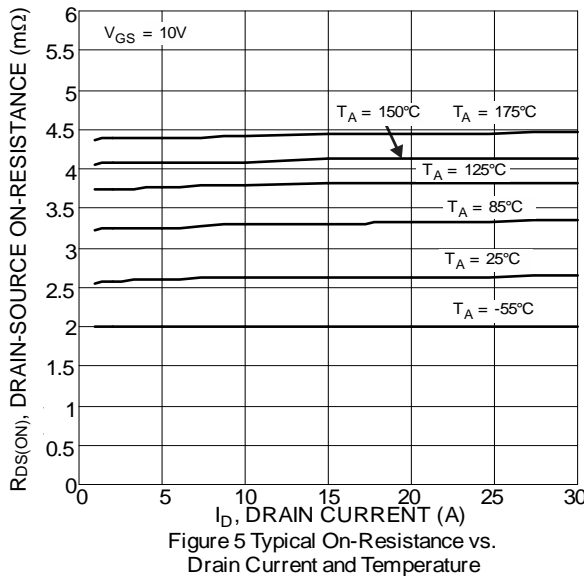
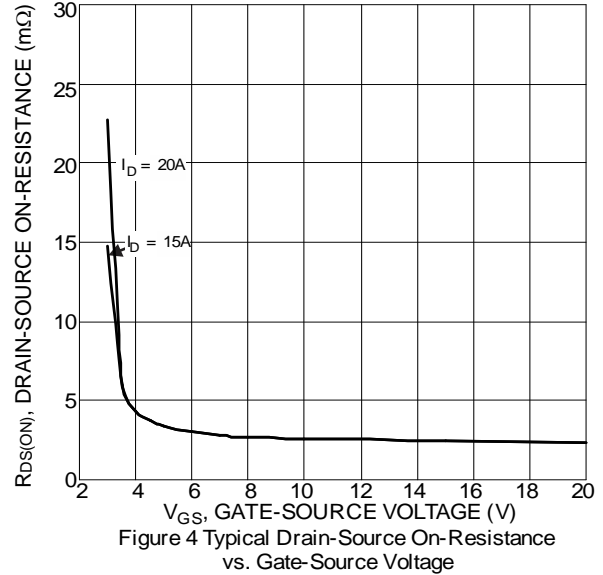
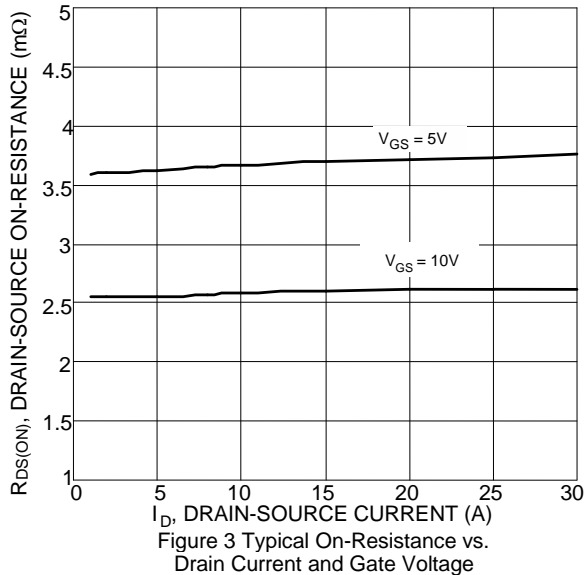
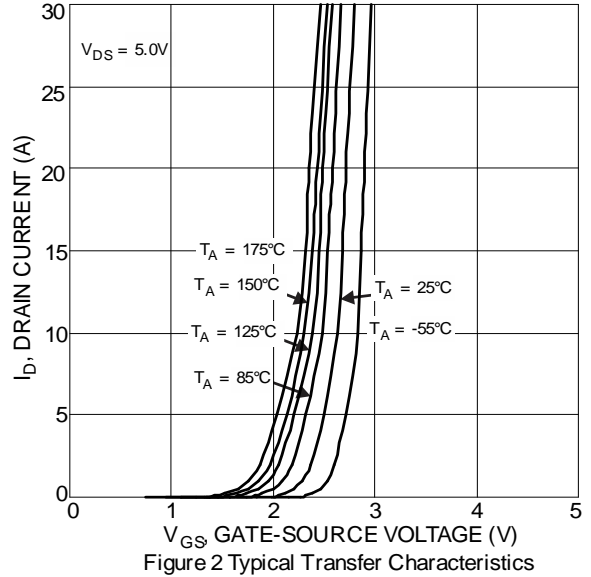
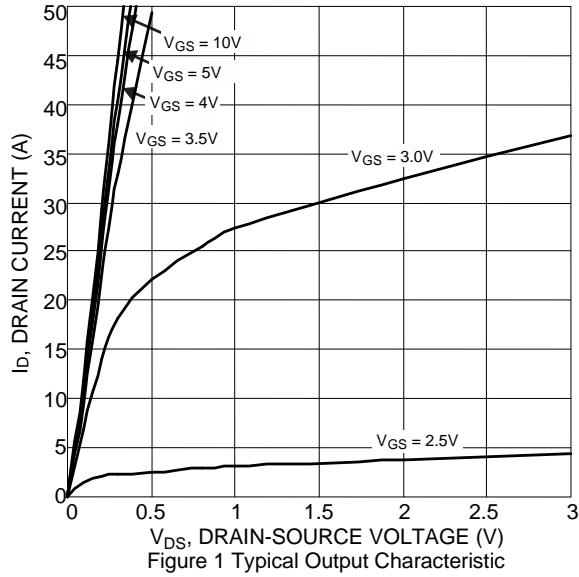
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.7	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	55	°C/W
Total Power Dissipation (Note 6)	P _D	83	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	1.8	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
6. Thermal resistance from junction to soldering point (on the exposed drain pad).
7. Package limit.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 32V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	2.7	3.3	mΩ	V _{GS} = 10V, I _D = 20A
		—	3.6	5.0		V _{GS} = 5V, I _D = 15A
Diode Forward Voltage	V _{SD}	—	—	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{ISS}	—	2693	—	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{OSS}	—	1172	—		
Reverse Transfer Capacitance	C _{ISS}	—	52	—		
Gate Resistance	R _G	—	2.54	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 10V)	Q _G	—	38.5	—	nC	V _{DS} = 20V, I _D = 20A
Total Gate Charge (V _{GS} = 4.5V)	Q _G	—	17.6	—		
Gate-Source Charge	Q _{GS}	—	6.9	—		
Gate-Drain Charge	Q _{GD}	—	6.9	—		
Turn-On Delay Time	t _{D(ON)}	—	5.2	—	ns	V _{DD} = 20V, V _{GS} = 10V, I _D = 20A, R _G = 3Ω
Turn-On Rise Time	t _R	—	5.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	23.5	—		
Turn-Off Fall Time	t _F	—	11	—		
Body Diode Reverse-Recovery Time	t _{RR}	—	35.4	—	ns	I _F = 20A, di/dt = 100A/μs
Body Diode Reverse-Recovery Charge	Q _{RR}	—	32.9	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.



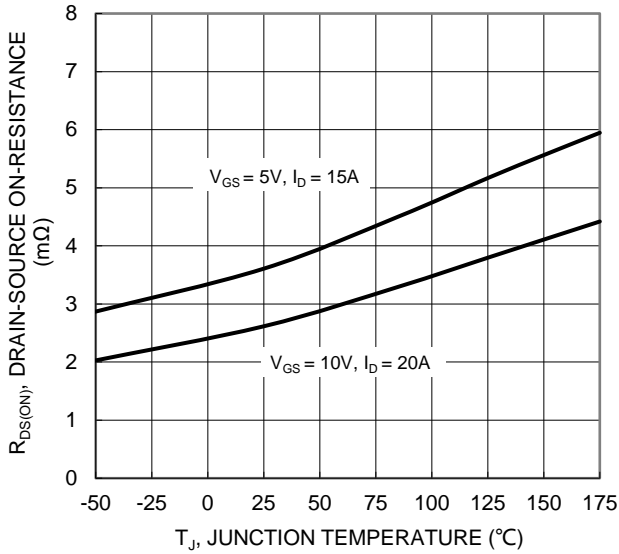


Figure 7 On-Resistance Variation with Temperature

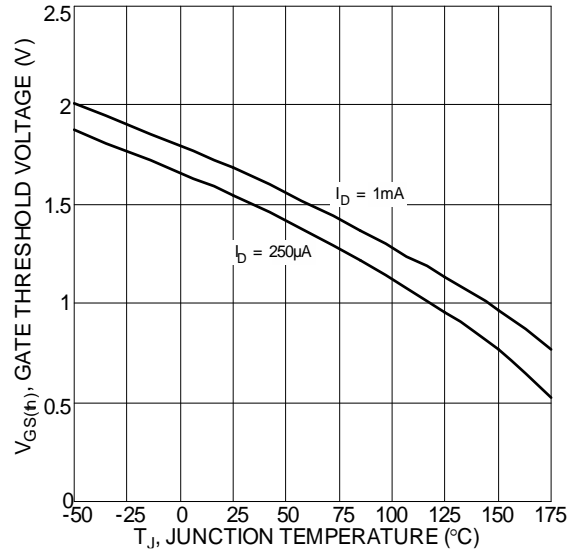


Figure 8 Gate Threshold Variation vs. Temperature

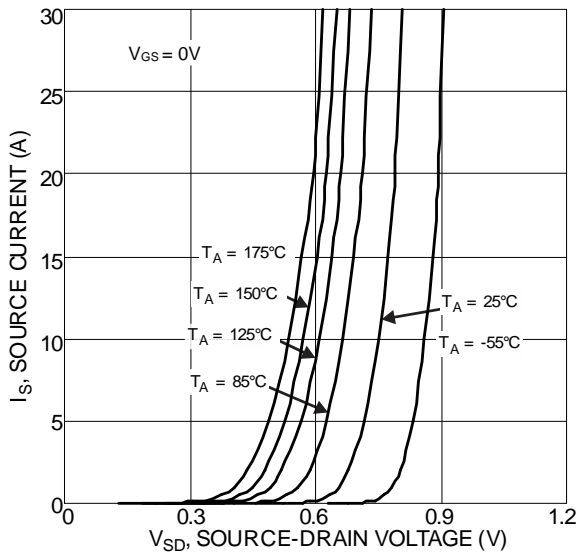


Figure 9 Diode Forward Voltage vs. Current

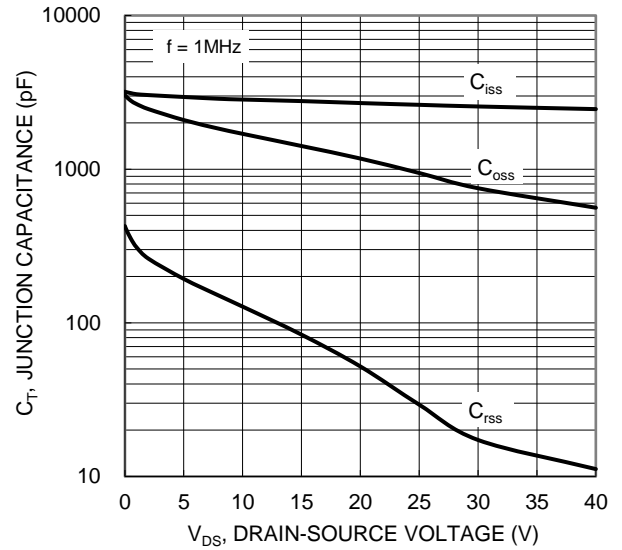


Figure 10 Typical Junction Capacitance

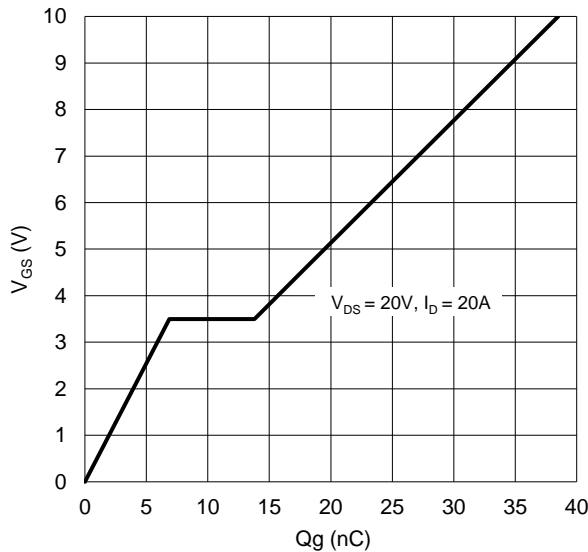


Figure 11 Gate Charge

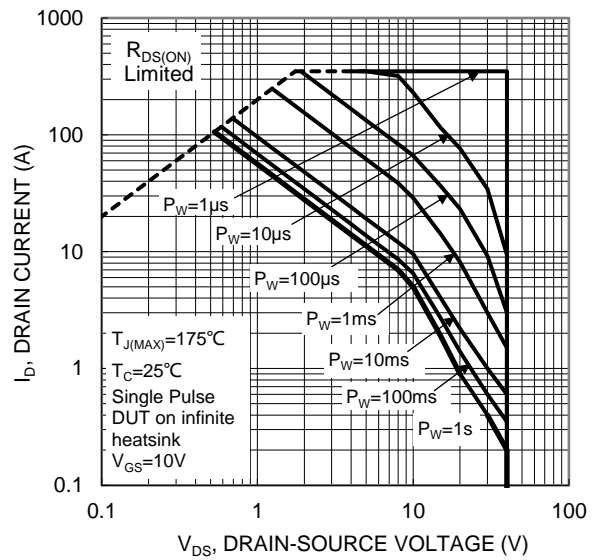
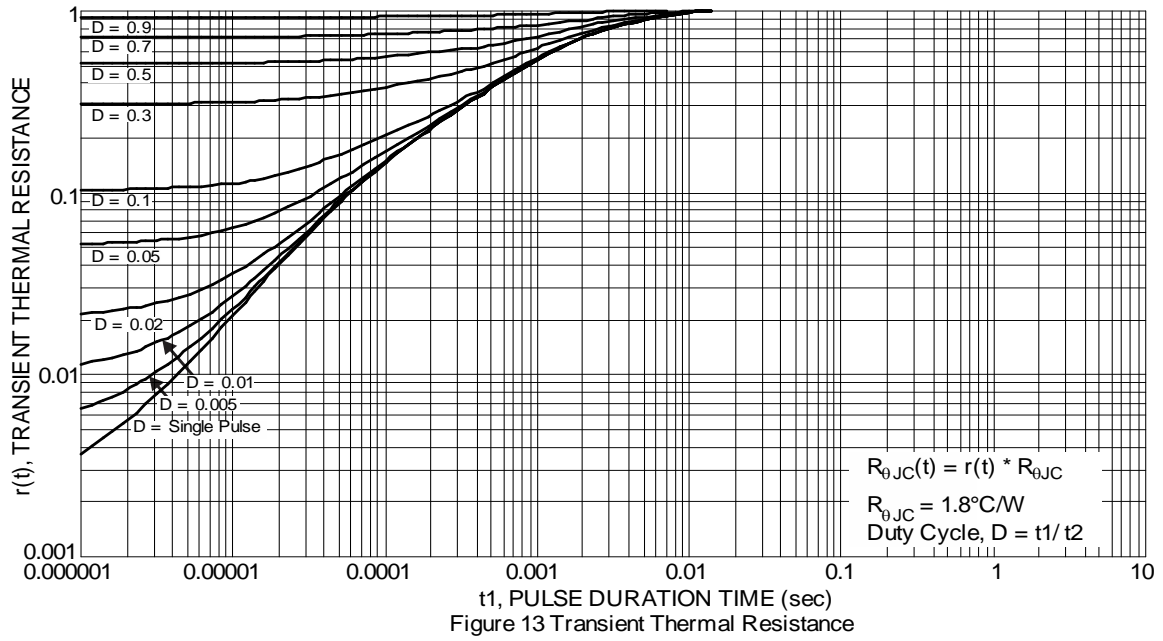


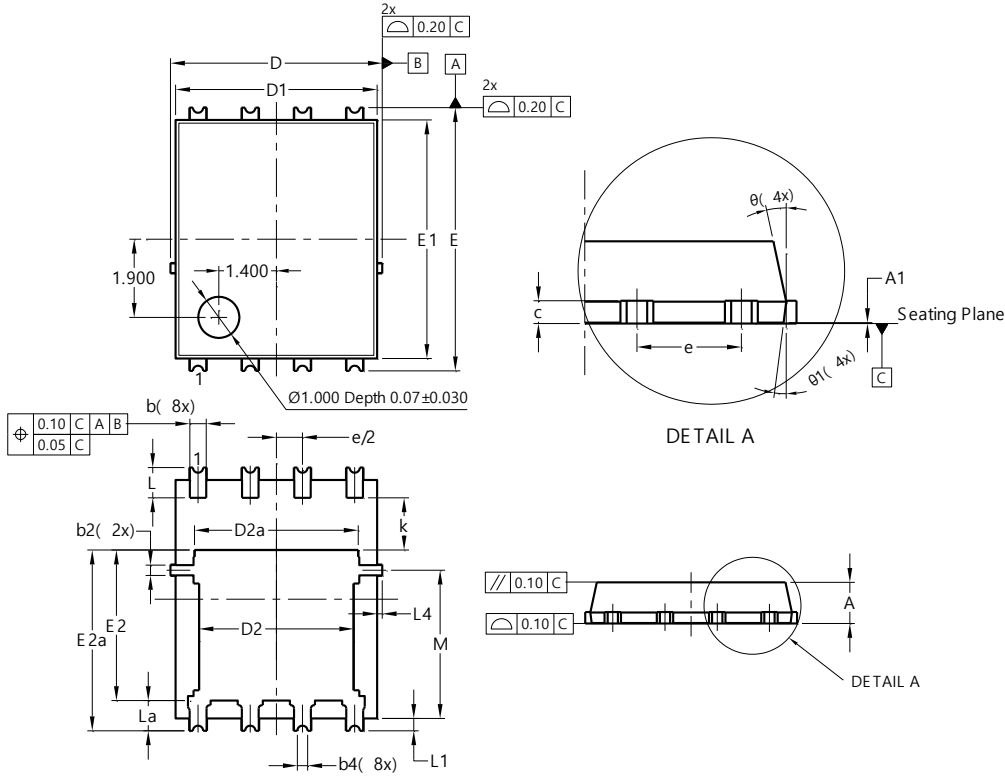
Figure 12 SOA, Safe Operation Area



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UX)

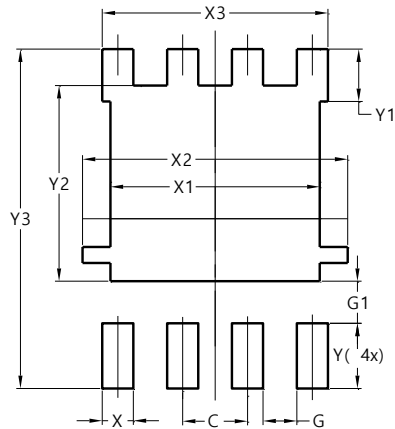


PowerDI5060-8/SWP (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	5.190
X3	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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