

40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C	
40V	5.5mΩ @ V _{GS} = 10V	86A	

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON) Minimizes Power Losses
- Wettable Flank for Improved Optical Inspection
- Fast Switching Speed
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH45M5SPSWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

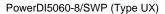
Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- High-frequency switching
- Synchronous rectification
- DC-DC converters

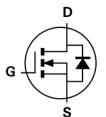
Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (2)
- · Weight: 0.097 grams (Approximate)

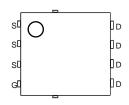




Top View Bottom View



Internal Schematic



Top View Pin Configuration

Ordering Information (Note 4)

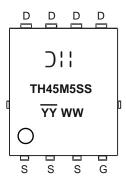
Orderable Part Number	Paskage	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMTH45M5SPSWQ-13	PowerDI5060-8/SWP (Type UX)	2500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



)|| = Manufacturer's Marking
TH45M5SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year Code (ex: 24 = 2024) WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	40	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current, $V_{GS} = 10V$ (Note 5) $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$		lo	86 60	А
Maximum Continuous Body Diode Forward Current (Note 5)	Is	86	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	344	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle =	Ism	344	Α	
Avalanche Current, L = 0.1mH	las	20.3	Α	
Avalanche Energy, L = 0.1mH	Eas	20.6	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	T _A = +25°C	PD	3.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	42	°C/W
Total Power Dissipation (Note 5)	T _C = +25°C	P _D	72	W
Thermal Resistance, Junction to Case (Note 5)	R ₀ JC	2.0	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

Notes:

- 5. Thermal resistance from junction to soldering point (on the exposed drain pad).6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	1	-	1	μΑ	V _{DS} = 32V, V _{GS} = 0V	
Gate-Source Leakage	Igss	1	1	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	VGS(TH)	2.0	1	3.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	3.9	5.5	mΩ	$V_{GS} = 10V, I_D = 25A$	
Diode Forward Voltage	V_{SD}	1	0.84	1.2	V	$V_{GS} = 0V, I_{S} = 25A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		1083	_		V _{DS} = 20V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	1	621	_	pF		
Reverse Transfer Capacitance	Crss	_	21	_			
Gate Resistance	R_g		1.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg		13.2	_		V _{DS} = 20V, I _D = 25A, V _{GS} = 10V	
Gate-Source Charge	Q_{gs}		4.2	_	nC		
Gate-Drain Charge	Q_{gd}	_	0.9	_			
Turn-On Delay Time	tD(ON)	_	5.4	_		$V_{GS} = 10V, V_{DD} = 20V$ $R_g = 3.5\Omega, I_D = 25A$	
Turn-On Rise Time	t _R	-	2.5	_	ns		
Turn-Off Delay Time	tD(OFF)	_	16.1	_	115		
Turn-Off Fall Time	tF	_	4.5	_			
Body Diode Reverse-Recovery Time	t _{RR}		61.3	_	ns	I _F = 25A, di/dt = 100A/µs	
Body Diode Reverse-Recovery Charge	Q_{RR}		52.1	_	nC	IF = 25A, αι/αι = 100A/μs	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.



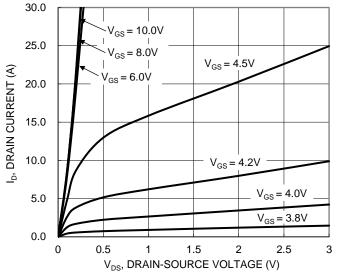


Figure 1. Typical Output Characteristic

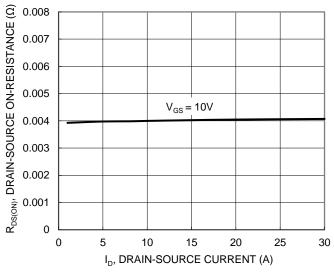


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

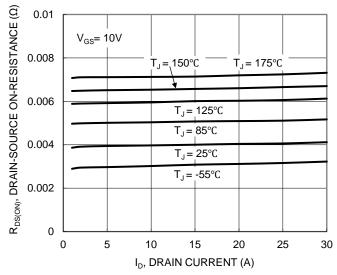


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

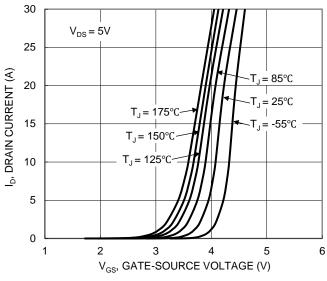


Figure 2. Typical Transfer Characteristic

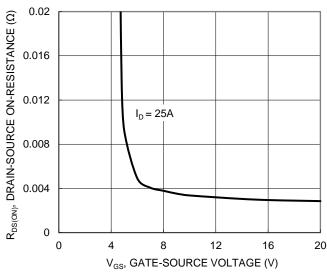


Figure 4. Typical Transfer Characteristic

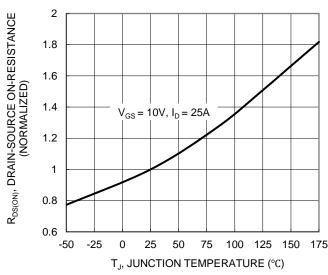


Figure 6. On-Resistance Variation with Junction Temperature





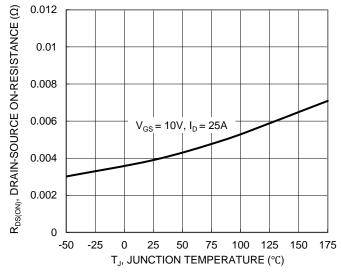
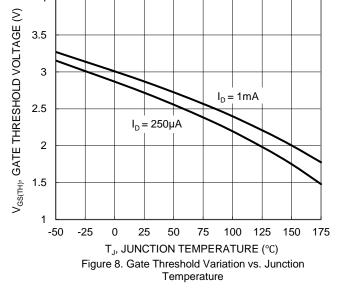


Figure 7. On-Resistance Variation with Junction Temperature



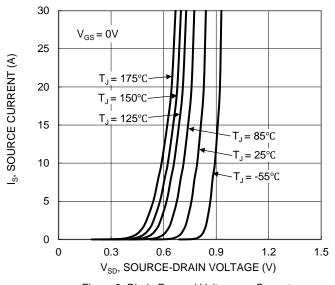


Figure 9. Diode Forward Voltage vs. Current

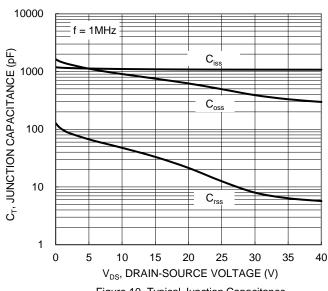


Figure 10. Typical Junction Capacitance

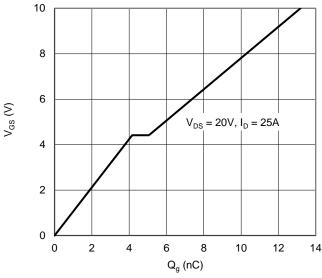


Figure 11. Gate Charge

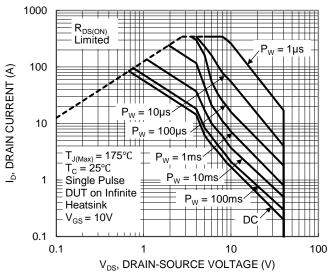


Figure 12. SOA, Safe Operation Area



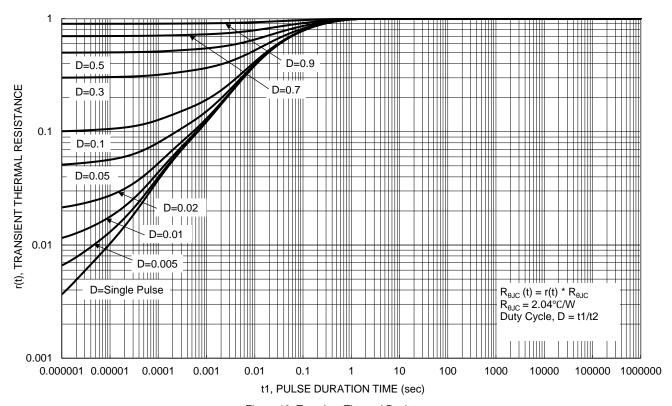


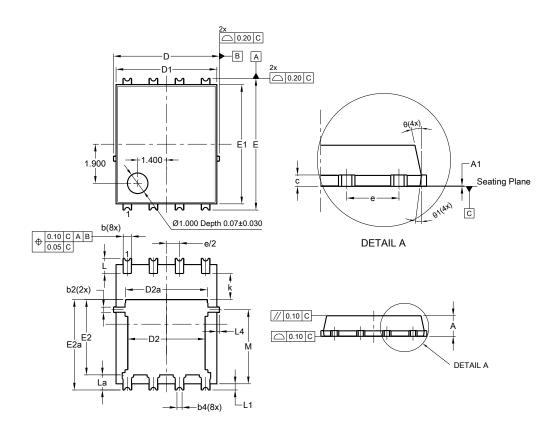
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UX)

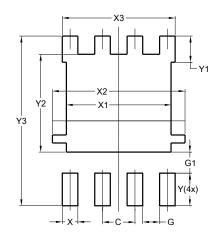


PowerDI5060-8/SWP				
(Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	C).25REF		
C	0.230	0.330	0.277	
D	5	.15 BS0	2	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6	.40 BS0		
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC		
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	4.100		
X2	5.190		
Х3	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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