



PowerDI5060-8

Product Summary

BV _{DSS}	Rds(on) max	I _{D MAX} T _C = +25°С (Note 9)
60V	6.5mΩ @ V _{GS} = 10V	100A
	10mΩ @ V _{GS} = 4.5V	81.6A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Engine management systems
- Body control electronics
- **DC-DC** converters

Features

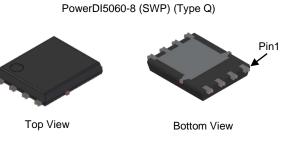
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application

60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

- Low RDS(ON) Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is gualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/guality/product-definitions/
- An automotive-compliant part is available under separate datasheet (DMTH6006LPSWQ)

Mechanical Data

- Package: PowerDI®5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)

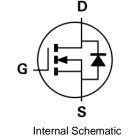


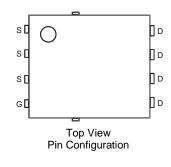


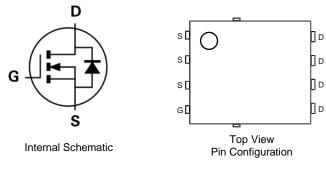
Top View



Bottom View







Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information (Note 4)

Orderable Part Number	Package	Packing		
		Qty.	Carrier	
DMTH6006LPSW-13	PowerDI5060-8 (SWP) (Type Q)	2500	Tape & Reel	
DMTH6006LPSW-13	PowerDI5060-8/SWP (Type UX)	2500	Tape & Reel	

Note: 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



D! = Manufacturer's Marking TH6006LSW = Product Type Marking Code YYWW or $\overline{YY}WW = Date Code Marking$ YY or \overline{YY} = Last Two Digits of Year (ex: 24 = 2024) WW = Week Code (01 to 53)

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		Vdss	60	V
Gate-Source Voltage		Vgss	±20	V
	T _A = +25°C		17.2	А
Continuous Drain Current, VGS = 10V (Note 5)	T _A = +100°C	lo	12.1	
	Tc = +25°C	ID	100	А
Continuous Drain Current, $V_{GS} = 10V$ (Notes 6 & 9)	T _C = +100°C		71.6	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		Ідм	400	А
Maximum Continuous Body Diode Forward Current (Note 7)		ls	100	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		lsм	400	А
Avalanche Current, L = 0.1mH		I _{AS}	28.5	А
Avalanche Energy, L = 0.1mH		Eas	40.7	mJ

 Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.
Limited by package. Notes:



Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.88	W
Thermal Resistance, Junction to Ambient (Note 5)		RθJA	52	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	100	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	1.5	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

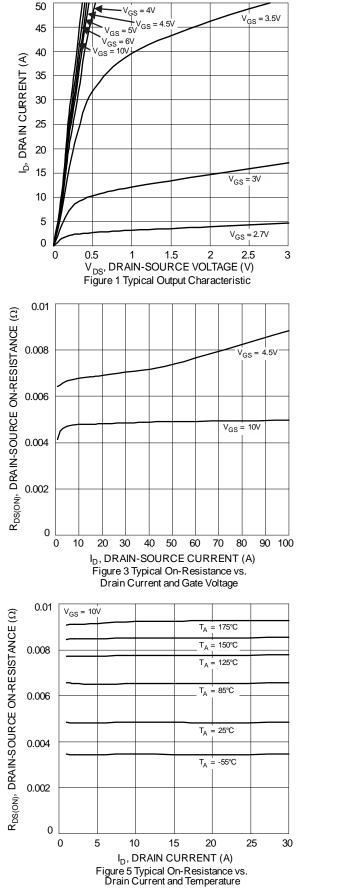
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

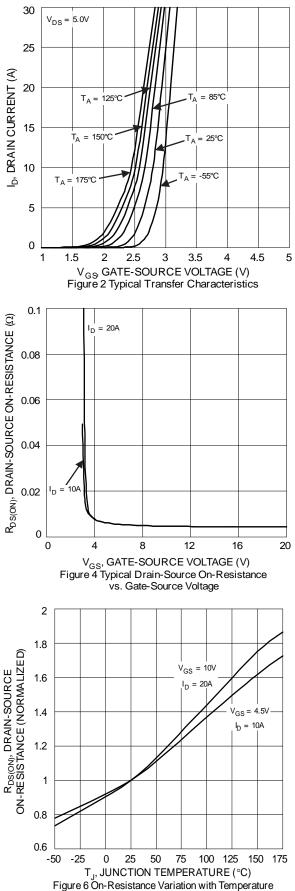
-			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	—		V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	IDSS	_	—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	1.2	—	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Decision		4.9	6.5	mΩ	VGS = 10V, ID = 20A
Static Drain-Source On-Resistance	RDS(ON)	_	7.1	10	11122	V _{GS} = 4.5V, I _D = 10A
Diode Forward Voltage	Vsd	_	0.8	1.2	V	VGS = 0V, IS = 20A
DYNAMIC CHARACTERISTICS (Note 8)						*
Input Capacitance	Ciss		2162	_	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	Coss		761	—		
Reverse Transfer Capacitance	Crss		58	—		
Gate Resistance	Rg	_	0.7	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	18.1	—		
Total Gate Charge (V _{GS} = 10V)	Qg	_	34.9	—	nC	VDS = 30V, ID = 20A
Gate-Source Charge	Qgs		6.1	—	nc	
Gate-Drain Charge	Q _{gd}		7.3	-		
Turn-On Delay Time	tD(ON)	—	6.0	—		
Turn-On Rise Time	tR	—	5.4	—		$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 20A, R_g = 3\Omega$
Turn-Off Delay Time	t _{D(OFF)}	_	20.4	—	ns	
Turn-Off Fall Time	tF	_	7.8	—		
Body Diode Reverse Recovery Time	trr		35.8	—	ns	
Body Diode Reverse Recovery Charge	Q _{RR}	_	40.2		nC	I _F = 20A, di/dt = 100A/µs

 Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad).
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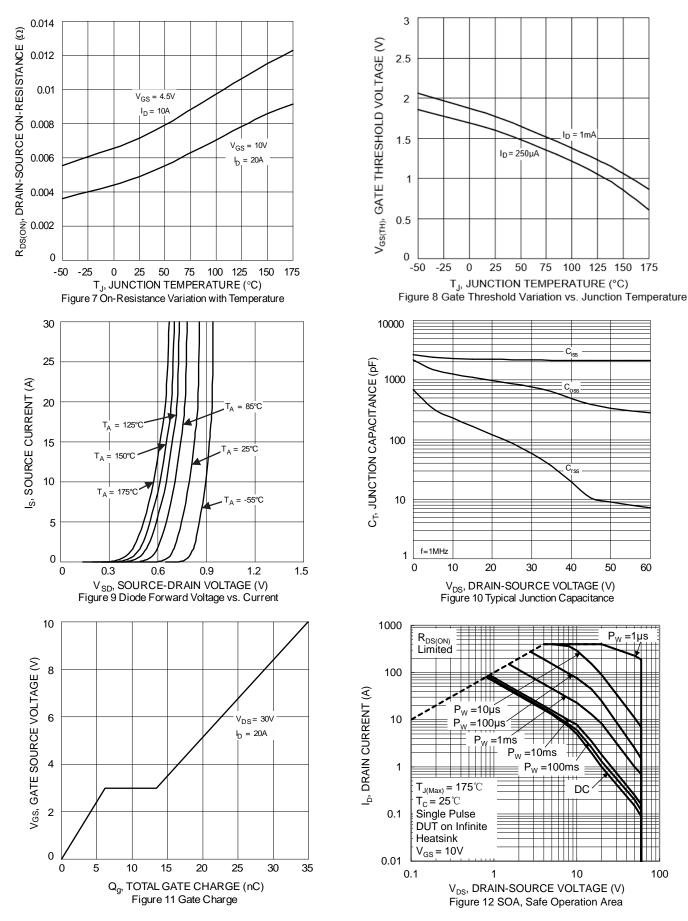


DMTH6006LPSW



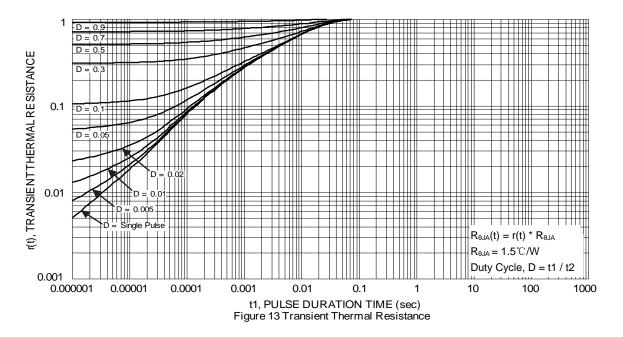






DMTH6006LPSW Document number: DS41532 Rev. 3 - 2







Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

e/2

b4(8x)

D2a

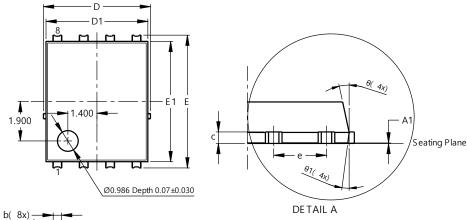
20

E 2 E2a

V

La

L1a(8x)



- b2(2x)

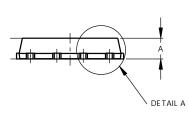
N 11

(Type Q)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	().25REF	-	
c	0.230	0.330	0.277	
D	5	.15 BS0	2	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6	.40 BS0	0	
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
e	1	.27BSC)	
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0	.050RE	F	
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All				

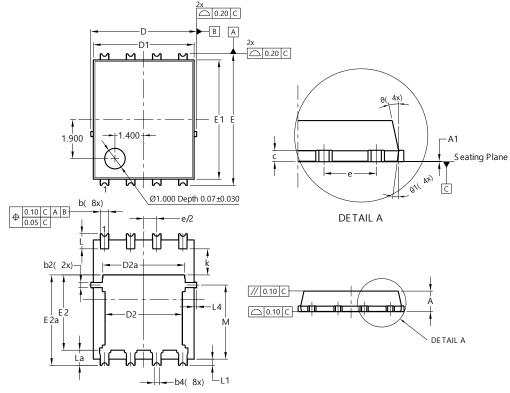
PowerDI5060-8 (SWP)

DETAIL A

PowerDI5060-8 (SWP) (Type Q)



PowerDI5060-8/SWP (Type UX)



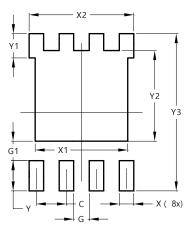
PowerDI5060-8/SWP					
	(Type UX)				
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0	0.05			
b	0.30	0.50	0.41		
b2	0.20	0.35	0.25		
b4	0).25REF	-		
С	0.230	0.330	0.277		
D	5	.15 BS0	C (
D1	4.70	5.10	4.90		
D2	3.56	3.96	3.76		
D2a	3.78	4.18	3.98		
E	6	.40 BS0	C (
E1	5.60	6.00	5.80		
E2	3.46	3.86	3.66		
E2a	4.195	4.595	4.395		
е	1	.27BSC)		
k	1.05				
L	0.635	0.835	0.735		
La	0.635	0.835	0.735		
L1	0.200	0.400	0.300		
L4	0.025	0.225	0.125		
Μ	3.205	4.005	3.605		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All	All Dimensions in mm				

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Suggested Pad Layout

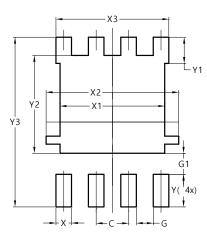
Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value
Dimensions	(in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

PowerDI5060-8 (SWP) (Type Q)

PowerDI5060-8/SWP (Type UX)



Dimensions	Value		
	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	5.190		
X3	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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