



DMWSH120H23SM4

1200V N-CHANNEL SILICON CARBIDE POWER MOSFET

Product Summary

BV _{DSS}	RDS(ON) Max	I _D Tc = +25°C		
1200V	23mΩ @V _{GS} = 18V	100A		

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

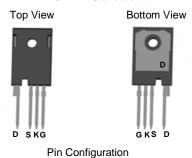
 https://www.diodes.com/quality/product-definitions/

Description and Applications

This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

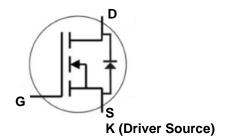
- SMPS (switching mode power supplies)
- UPS (uninterruptable power supplies)
- Energy storages
- EV charging systems
- Solar inverters

TO247-4 Standard



Mechanical Data

- Package: TO247-4
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- Weight: 6.6 grams (Approximate)



Internal Schematic

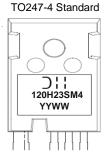
Ordering Information (Note 4)

Dord Number	Don't Museliness	Packing			
Part Number	Package	Qty.	Carrier		
DMWSH120H23SM4	TO247-4 Standard	30 Pieces	Tube		

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



D: I = Manufacturer's Marking
120H23SM4 = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 24 = 2024)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Value	Unit
Drain-Source Voltage		V_{DSS}	1200	V
Gate-Source Voltage		Vgs	-10/+22	V
Gate-Source Voltage (recommended operating values)		V _{GS}	-4/+18	V
Gate-Source Transient Voltage, tP < 1µs, t ≤ 10 Hours over Lifetime		V_{GS}	-11/+25	V
Continuous Drain Current (Notes 5, 6)	$T_C = +25$ °C $T_C = +100$ °C	I _D	100 71	А
Continuous Diode Forward Current (Note 5)			76	Α
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		lsм	280	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		IDM	280	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _C = +25°C	- P _D	349	- W	
Total Power Dissipation (Note 5)	$T_C = +100^{\circ}C$		174		
Thermal Resistance, Junction to Ambient (Note 7)			26.6	°C/W	
Thermal Resistance, Junction to Case (Note 5)		Rejc	0.43	C/VV	
Operating and Storage Temperature Range			-55 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	1200	_	_	V	V _G S = 0, I _D = 100μA	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	50	μΑ	V _{DS} = 1200V, V _{GS} = 0	
Gate-Source Leakage	Igss	_	_	±250	nA	$V_{GS} = +18/-4V, V_{DS} = 0$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.8	2.5	3.6	V	$V_{DS} = V_{GS}$, $I_D = 17.7mA$	
Static Drain-Source On-Resistance	RDS(ON)	_	17	23	mΩ	V _G S = 18V, I _D = 50A	
Static Drain-Source On-Resistance	RDS(ON)	-	20	_	mΩ	V _{GS} = 15V, I _D = 50A	
Diode Forward Voltage	V_{SD}	_	3.8	_	V	$V_{GS} = -4V$, $I_{S} = 25A$	
Transconductance	gfs	_	13	_	S	V _{DS} = 20V, I _D = 50A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	3962	_		V _{GS} = 0, V _{DS} = 1000V V _{AC} = 25mV, f = 1MHz	
Output Capacitance	Coss	_	184	_	pF		
Reverse Transfer Capacitance	Crss	-	9.1	_			
Coss Stored Energy	Eoss		110	_	μJ		
Turn-On Switching Energy (Body Diode Forward)	Eon		676	_	1	$V_{GS} = -4V/+18V$, $V_{DS} = 800V$ $R_g = 5\Omega$, $I_D = 50A$, $L = 157\mu H$	
Turn-Off Switching Energy (Body Diode Forward)	Eoff	_	486	_	μJ		
Gate Resistance	Rg	1	1.2	_	Ω	V _{AC} = 100mV, f = 1MHz	
Total Gate Charge	Qg		217	_		Vgs = -4V/+18V, Vps = 800V Ip = 50A	
Gate-Source Charge	Qgs		60.4	_	nC		
Gate-Drain Charge	Q_{gd}		53.4	_			
Turn-On Delay Time	td(ON)	_	21.0	_	ns	$\begin{aligned} &\text{VGS} = \text{-4V/+18V}, \text{V}_{\text{DS}} = 800\text{V} \\ &\text{R}_g = 5\Omega, \text{I}_D = 50\text{A} \\ &\text{Inductive Load} \end{aligned}$	
Turn-On Rise Time	t _R	-	39.5	_			
Turn-Off Delay Time	t _{D(OFF)}	_	51.4	_			
Turn-Off Fall Time	tF	_	14.3	_			
Body Diode Reverse-Recovery Time	trr	_	23.1	_	ns	1/ 0000/	
Body Diode Reverse-Recovery Charge	Q _{RR}	_	424	_	nC	V _{GS} = -4V, V _{DS} = 800V I _D = 50A, di/dt = 2600A/µs	
Body Diode Reverse-Recovery Current	IRRM	_	29.7	_	Α	10 – 30A, αι/αι = 2000A/μS	

Notes:

- 5. Device mounted on an infinite heatsink.
- 6. Drain current limited by maximum junction temperature.
- 7. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.



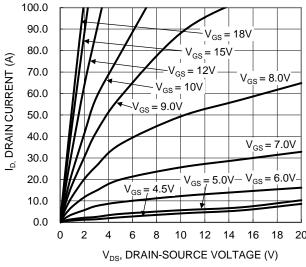


Figure 1. Typical Output Characteristic

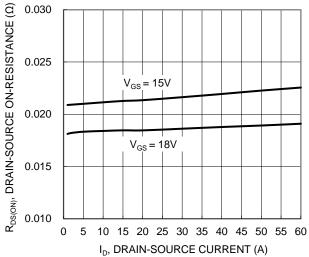


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

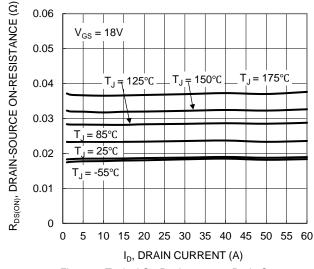


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

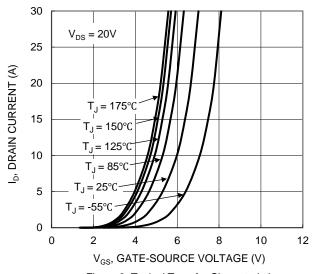


Figure 2. Typical Transfer Characteristic

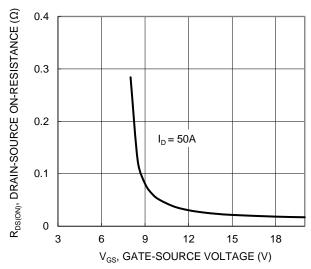


Figure 4. Typical Transfer Characteristic

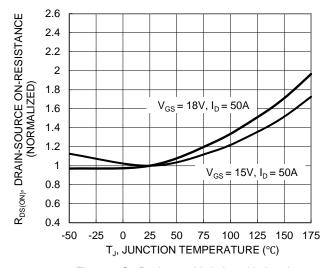


Figure 6. On-Resistance Variation with Junction Temperature



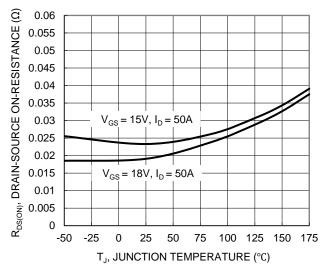
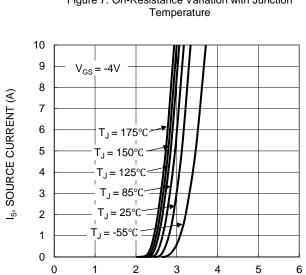
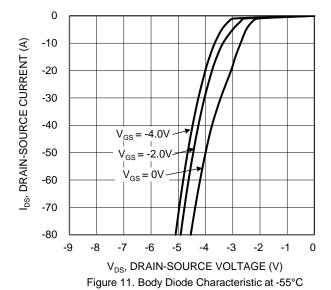


Figure 7. On-Resistance Variation with Junction Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



4 $V_{\text{GS(TH)}},$ GATE THRESHOLD VOLTAGE (V) 3.5 $I_D = 5mA$ 3 $I_D = 17.7 \text{mA}$ 2.5 2 $I_{D} = 250 \mu A$ 1.5 $I_D = 1 \text{mA}$ 1 0.5 0 -50 -25 0 25 50 75 100 125 150 175 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs Junction Temperature

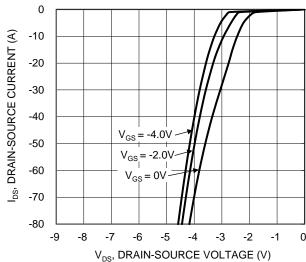


Figure 10. Body Diode Characteristic at 25°C

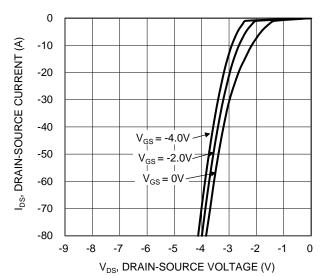
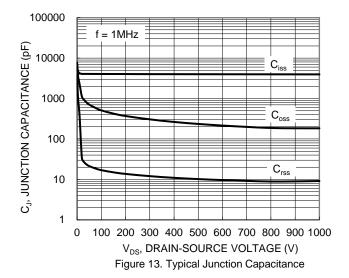
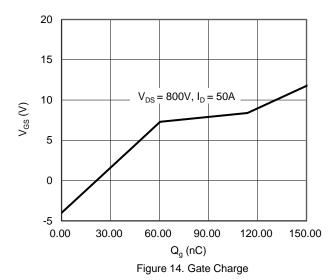
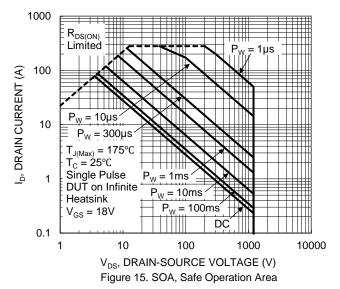


Figure 12. Body Diode Characteristic at 175°C









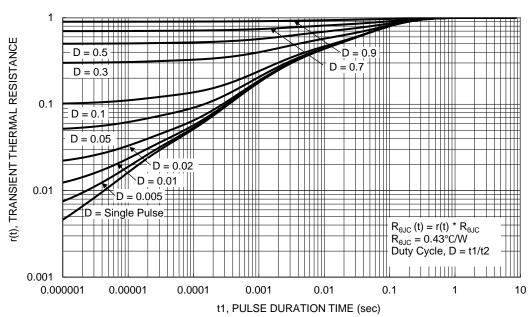


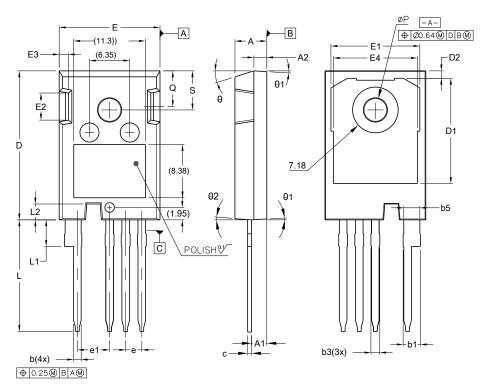
Figure 16. Transient Thermal Resistance



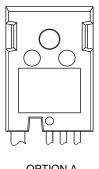
Package Outline Dimensions

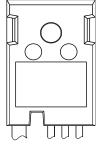
Please see http://www.diodes.com/package-outlines.html for the latest version.

TO247-4 Standard



TO247-4 Standard				
Dim	Min	Max		
Α	4.83	5.21		
A1	2.29	2.54		
A2	1.91	2.16		
b	1.07	1.33		
b1	2.39	2.94		
b3	1.07	1.60		
b5	2.39	2.69		
С	0.55	0.68		
D	23.30	23.60		
D1	16.25	17.65		
D2	0.95	1.25		
Е	15.75	16.30		
E1	13.10	14.15		
E2	3.68	5.10		
E3	1.00	1.90		
E4	12.38	13.43		
е	2.54 BSC			
e1		BSC		
L	17.31	17.82		
L1 L2	3.97	4.37		
L2	2.35	2.65		
ØP	3.51	3.65		
Q	5.49	6.00		
S	6.04	6.30		
θ	17.5°- 20° REF			
θ1	3.5°- 5° REF			
θ2	4°- 5° REF			
All Dimensions in mm				





OPTION A (TOP VIEW)

OPTION B (TOP VIEW)



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