



#### **DUAL PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR**

### **Features**

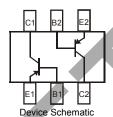
- Epitaxial Planar Die Construction
- Small Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

### **Mechanical Data**

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe.
  Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.016 grams (approximate)



Top View



## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-60	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-50	V
Emitter-Base Voltage	$V_{EBO}$	-5.0	V
Continuous Collector Current	Ic	-500	mA

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient Air (Note 3) @T <sub>A</sub> = 25°C	$R_{ heta JA}$	417	°C /W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-60	_	_	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-50	_	-	V	I <sub>C</sub> = -1.0mA
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5.0	_	_	V	I <sub>E</sub> = -100μA
Collector Cutoff Current	I <sub>CBO</sub>	_	_	-0.1	μА	V <sub>CB</sub> = -30V
Emitter Cutoff Current	I <sub>EBO</sub>	_	_	-0.1	μА	V <sub>EB</sub> = -4.0V
ON CHARACTERISTICS (Note 4)						
DC Current Gain	h <sub>FE</sub>	120	_	390		$V_{CE} = -3.0V, I_{C} = -100mA$
Collector-Emitter Saturation Voltage (Note 3)	$V_{CE(SAT)}$	_		-0.6	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS						
Gain Bandwidth Product	$f_{T}$	_	200	_	MHz	V <sub>CE</sub> = -5V, I <sub>E</sub> = 20mA, f = 100MHz
Output Capacitance	C <sub>ob</sub>	_	7	_	pF	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 3. Device mounted on FR-4 PCB; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on page 4 or on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Short duration pulse test used to minimize self-heating effect.



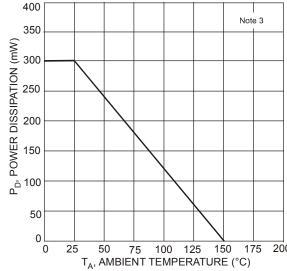
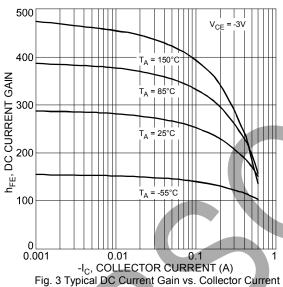
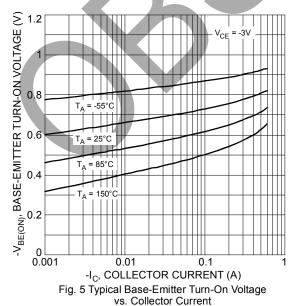


Fig. 1, Max Power Dissipation vs. Ambient Temperature





0.50 -I<sub>C</sub>, COLLECTOR CURRENT (A) 0.40 I<sub>B</sub> = -10mA I<sub>B</sub> = -8mA 0.30 I<sub>B</sub> = -6mA 0.20 I<sub>B</sub> = -4mA 0.10  $I_B = -2mA$ 0.00 -V<sub>CE</sub>, COLLECTOR EMITTER VOLTAGE (V)

Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

-V<sub>CE(SAT)</sub>, COLLECTOR EMITTER. SATURATION VOLTAGE (V)  $T_A = -55^{\circ}C$ 0.01 0.01 0.1

-I<sub>C</sub>, COLLECTOR CURRENT (A) Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

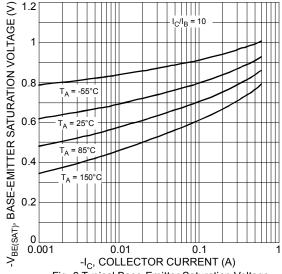
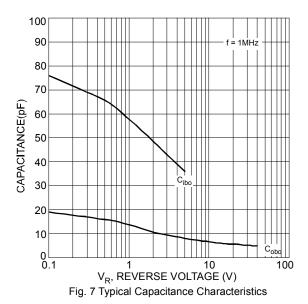


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current





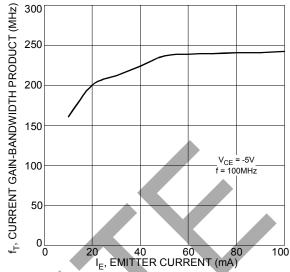


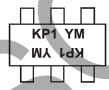
Fig. 8 Typical Gain-Bandwidth Product vs. Emitter Current

## Ordering Information (Note 5)

Part Number	Case	Packaging
IMT17-7	SOT-26	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**

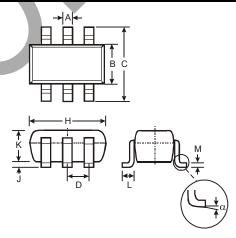


KP1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key

Year	2007	20	008	2009	2010	20	11	2012	2013	20	14	2015
Code	U		V	W	X	,	Y	Z	Α	[	3	С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

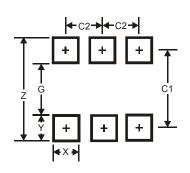
## **Package Outline Dimensions**



SOT-26					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	_	_	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
α	0°	8°	_		
All D	All Dimensions in mm				



# Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95





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