

MCR100-8(LS)

SENSITIVE GATE SILICON CONTROLLED RECTIFIERS REVERSE BLOCKING THYRISTORS

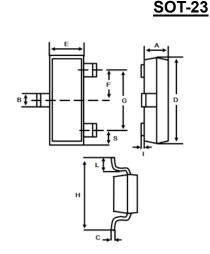
SCRs 0.25 AMPERES RMS 600 VOLTS

FEATURES

- Sensitive gate allows triggering by microcontrollers and other logic circuits.
- Blocking voltage to 600 volts.
- On-state current rating of 0.25 amperes RMS at +80°C.
- High surge current capability 9 Amperes.
- Minimum and maximum values of I_{GT} , V_{GT} and I_{H} specified for ease of design.
- Immunity to $dv/dt 20V/\mu s$ minimum at $T_J = +110$ °C
- Glass-passivated surface for reliability and uniformity.
- Autoclave test meets JESD22-A102-C, condition B.
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

MECHANICAL DATA

- Package: SOT-23
- · Package Material: molding plastic. Pb-Free package
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.007 ounces, 0.2 grams (Approximate)



| SOT-23 | | | | | |
|--------|-----------|------|--|--|--|
| DIM. | MIN. MAX. | | | | |
| Α | 0.89 | 1.20 | | | |
| В | 0.30 | 0.51 | | | |
| С | 0.085 | 0.18 | | | |
| D | 2.75 | 3.04 | | | |
| Е | 1.20 | 1.60 | | | |
| F | 0.85 | 1.05 | | | |
| G | 1.70 | 2.10 | | | |
| Н | 2.10 2.75 | | | | |
| ı | 0.0 | 0.1 | | | |
| L | 0.60 TYP. | | | | |
| S | 0.35 | 0.65 | | | |



| PIN | PIN ASSIGNMENT | | | | | |
|-----|----------------|--|--|--|--|--|
| 1 | Gate | | | | | |
| 2 | Cathode | | | | | |
| 3 | Anode | | | | | |

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS ($T_J = +25^{\circ}C$, unless otherwise noticed.) ABSOLUTE RATINGS

| CHARACTERISTICS | SYMBOL | VALUE | UNIT |
|---|--------------------------------------|-------------|------------------|
| Peak repetitive off-state voltage (Note 4) $T_J = -40$ °C to +110 °C, sine wave, 50 to 60 H_Z , gate open | V _{DRM} V _{RRM} | 600 | V |
| On-state RMS current (T _C = +80°C) 180°c conduction angels | I _{T(RMS)} | 0.25 | Α |
| Peak non-repetitive surge current 1/2 cycle sine wave 60H _Z @ T _J = +25°C | I _{TSM} | 9 | Α |
| Circuit fusing consideration @ t = 8.3ms | l²t | 0.336 | A ² s |
| Forward peak gate power, pulse width ≦ 1.0µs @ Ta = +25°C | P _{GM} | 0.1 | W |
| Forward average gate power, t ≤ 8.3ms @ Ta = +25°C | $P_{G(AV)}$ | 0.1 | W |
| Forward peak gate current, Pulse width ≤ 1.0µs @ Ta = +25°C | I _{GM} | 1 | А |
| Reverse peak gate voltage, Pulse width ≤ 1.0ms @ Ta = +25°C | V_{GRM} | 5 | V |
| Operating Junction Temperature Range | TJ | -40 to +110 | °C |
| Storage Temperature Range | T _{STG} | -40 to +150 | °C |

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



THERMAL CHARACTERISTICS

| CHARACTERISTIC | SYMBOL | VALUE | UNIT |
|--|--------|-------|------|
| Thermal resistance – junction to case | | 50 | °C/W |
| Maximum lead temperature for soldering purposes 1/16" from case for 10 seconds | TL | 260 | °C |

ELECTRICAL CHARACTERISTICS (T_J = +25°C, unless otherwise noted.)

| OFF CHARACTERISTICS | | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|--|-----------------------|--------|------|------|------|------|
| Peak repetitive forward or reverse blocking current | T _J = 25°C | IDRM | | | 10 | |
| ($V_D = R_{ated} V_{DRM}$ and V_{RRM} ; $R_{GK} = 1K$ ohms) | $T_J = 110^{\circ}C$ | IRRM | | | 100 | μΑ |

| ON CHARACTERISTICS | | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|---|---|--------|------|------|------------|------|
| Peak forward on-state voltage (I_{TM} = ± 0.3 A peak, pulse width ≤ 1.0 ms, duty cycle $\leq 1\%$) | | Vтм | | | 1.5 | V |
| Gate trigger current ($V_D = 7.0Vdc$, $R_L = 100 Ohms$) | | lgт | | | 50 | μΑ |
| Holding current (V _D = 7.0Vdc, R _L =100 Ohms) | $T_J = 25^{\circ}C$ $T_J = -40^{\circ}C$ | IH | | | 5 10 | mA |
| Gated trigger voltage(V_D = 7.0Vdc, RL= 100 Ohms) | $T_J = 25^{\circ}C$ $T_J = -40^{\circ}C$ | Vgt | | | 0.8 1.2 | V |
| Latch current (V _D = 7.0Vdc, R _L = 100 Ohms) | $T_J = 25^{\circ}C$ $T_J = -40^{\circ}C$ | IL | | | 10 15 | mA |

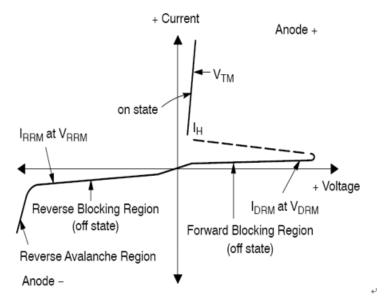
| DYNAMIC CHARACTERISTICS | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|--|--------|------|------|------|------|
| Critical rate of rise of off-state voltage (V_D = rated V_{DRM} , exponential waveform, R_{GK} = 1k Ohms, T_J =110°C) | dv/dt | 20 | | | V/µs |
| Critical rate of rise of on-state current $(I_{PK} = 50A, P_W = 10 usec, f = 60H_Z)$ | di/dt | | | 50 | A/µs |



RATING AND CHARACTERISTIC CURVES MCR100-8

Voltage Current Characteristic of SCR

| Symbol | Parameter |
|------------------|---|
| V _{DRM} | Peak Repetitive Off State Forward Voltage |
| I _{DRM} | Peak Forward Blocking Current |
| V _{RRM} | Peak Repetitive Off State Reverse Voltage |
| I _{RRM} | Peak Reverse Blocking Current |
| V _{TM} | Peak on State Voltage |
| I _H | Holding Current |





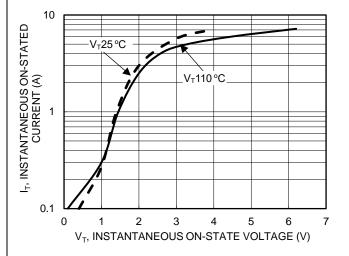
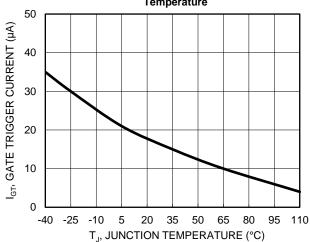
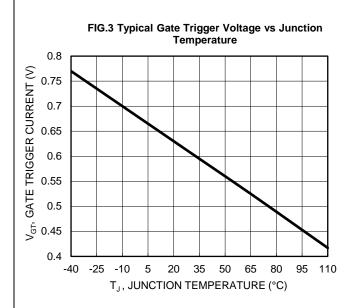


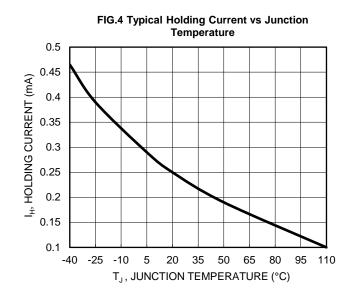
FIG.2 Typical Gate Trigger Current vs Junction Temperature

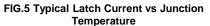




RATING AND CHARACTERISTIC CURVES MCR100-8







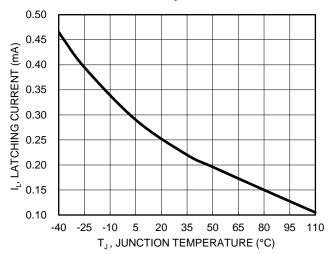
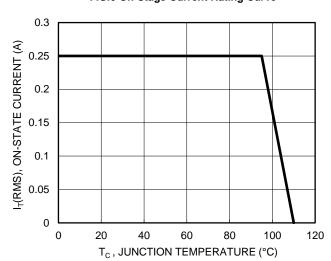


FIG.6 On-Stage Current Rating Curve

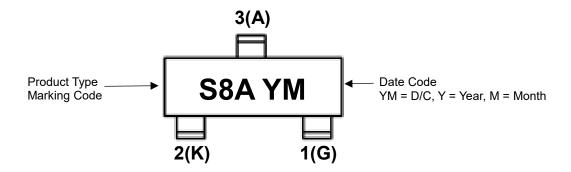




Ordering Information:

| Part Number | Packago | Packing | | |
|-------------|---------|---------|---------|--|
| Fait Number | Package | Qty. | Carrier | |
| MCR100-8 | SOT-23 | 3000 | T&R | |

Marking Information:





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