NOT RECOMMENDED FOR NEW DESIGN
CONTACT US

Lead-free Green

MMBD5004BRM

HIGH VOLTAGE SURFACE MOUNT SWITCHING DIODE ARRAY

Features

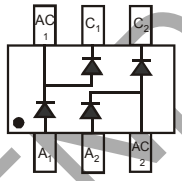
- Two Series Diode Circuits Connect to Form Full Wave Bridge
- Fast Switching Speed
- Low Capacitance
- 400V Reverse Breakdown Voltage Rating
- **Totally Lead Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 e3
- Polarity: See Diagram
- Weight: 0.016 grams (Approximate)

SOT-26

TOP VIEW



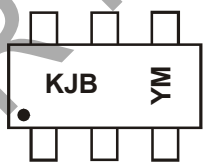
TOP VIEW
Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
MMBD5004BRM-7	SOT-26	3,000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Please see http://www.diodes.com/package-outlines.html for the latest version.

Marking Information



KJB = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: W = 2009)
M = Month (ex: 9 = September)

Date Code Key

Year	2009	2010	…	2016	2017	2018	2019
Code	W	X	…	D	E	F	G

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Repetitive Peak Reverse Voltage		V_{RRM}	400	V
Working Peak Reverse Voltage DC Blocking Voltage		V_{RWM} V_R	350	V
RMS Reverse Voltage		$V_{R(RMS)}$	247	V
Forward Continuous Current		I_F	225	mA
Peak Repetitive Forward Current		I_{FRM}	625	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0ms @ t = 1.0s	I_{FSM}	2.0 1.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	350	mW
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{\theta JA}$	357	°C/W
Operating and Storage Temperature Range	T_J,T_{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	400	—	—	V	I_R = 150µA
Forward Voltage	V_F	—	— — —	0.93 1.10 1.29	V	I_F = 20mA I_F = 100mA I_F = 200mA
Reverse Current (Note 6)	I_R	—	— — —	100 100 5	nA µA µA	V_R = 240V V_R = 240V, T_J = 150°C V_R = 360V
Total Capacitance	C_T	—	0.7	2.0	pF	V_R = 0V, f = 1.0MHz
Reverse Recovery Time	t_{rr}	—	—	50	ns	I_F = I_R = 30mA, I_{rr} = 3.0mA, R_L = 100Ω

Notes:
5. Part mounted on polyimide substrate PC board with recommended pad layout, which can be found on our website at http://www.diodes.com/package- outlines.html.
6. Short duration pulse test used to minimize self-heating effect.

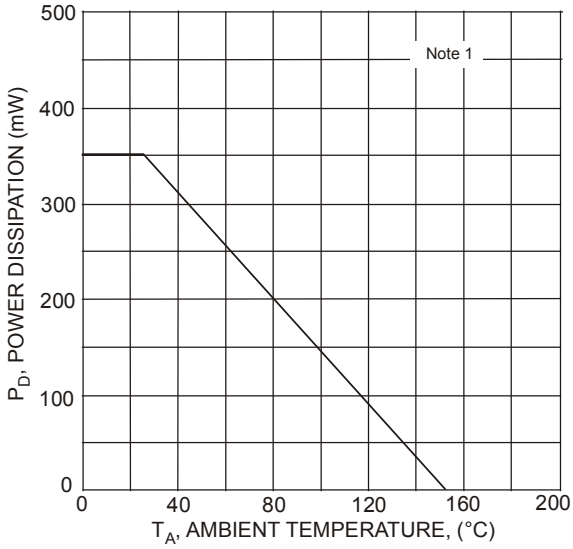


Fig. 1 Power Derating Curve, Total Package

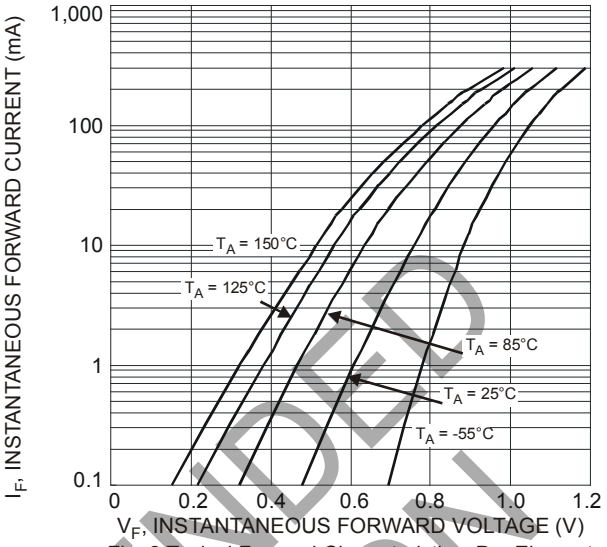


Fig. 2 Typical Forward Characteristics, Per Element

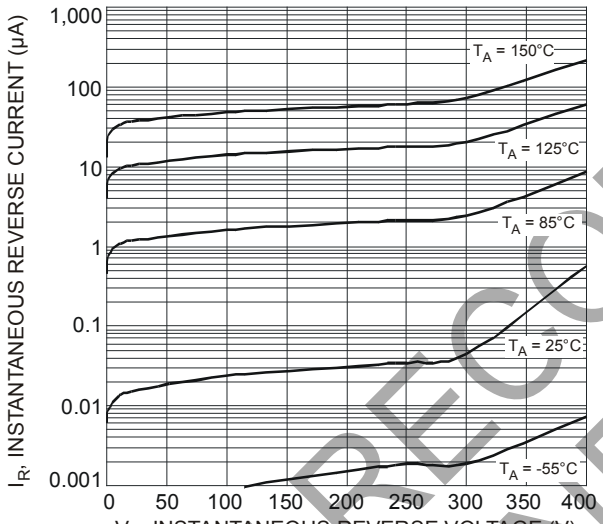


Fig. 3 Typical Reverse Characteristics, Per Element

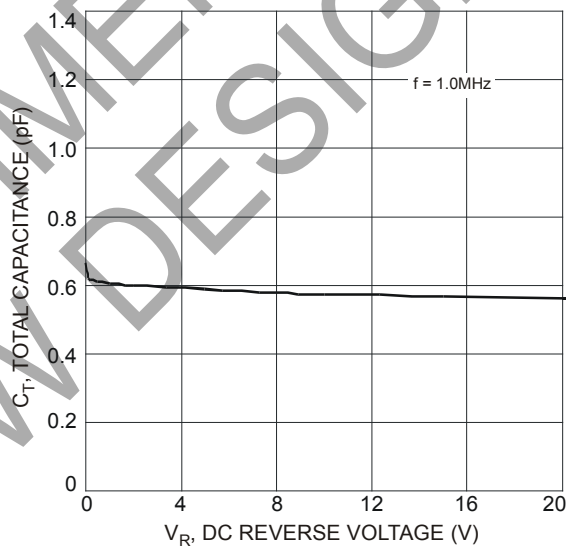
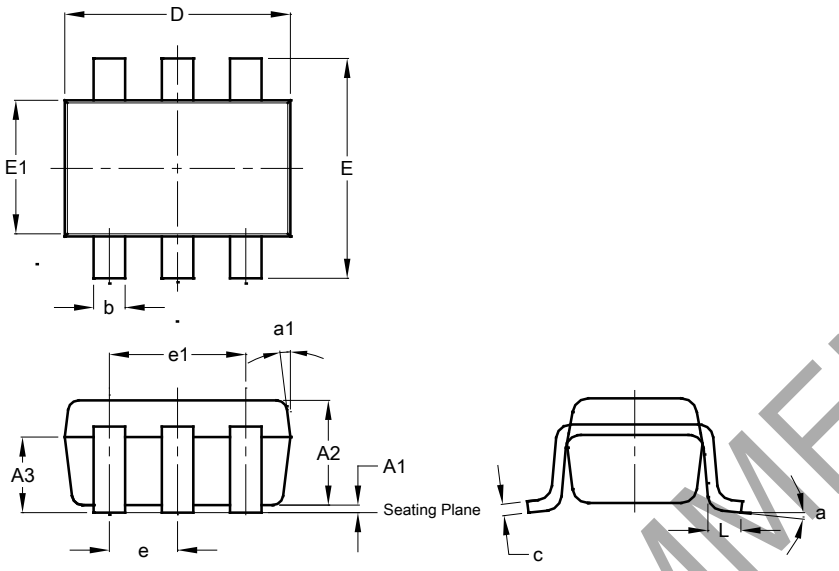


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Package Outline Dimensions

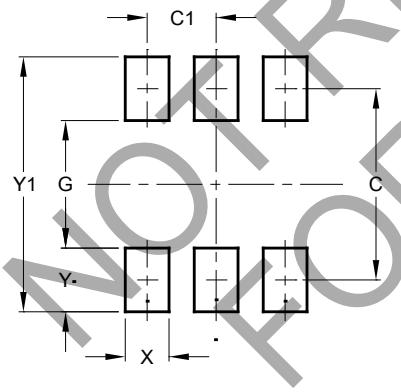
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT26 / SC74R			
Dim	Min	Max	Typ
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	-	-	0.95
e1	-	-	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	-	-	8°
a1	-	-	7°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20

Application Examples

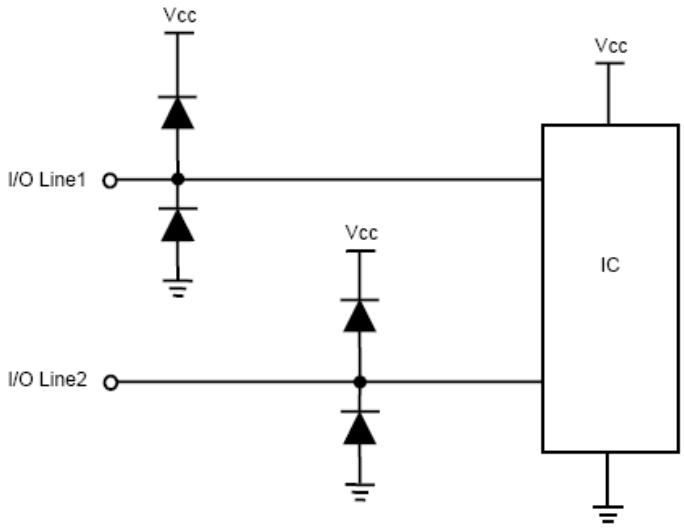


Figure 1. Typical Rail-to-Rail Protection

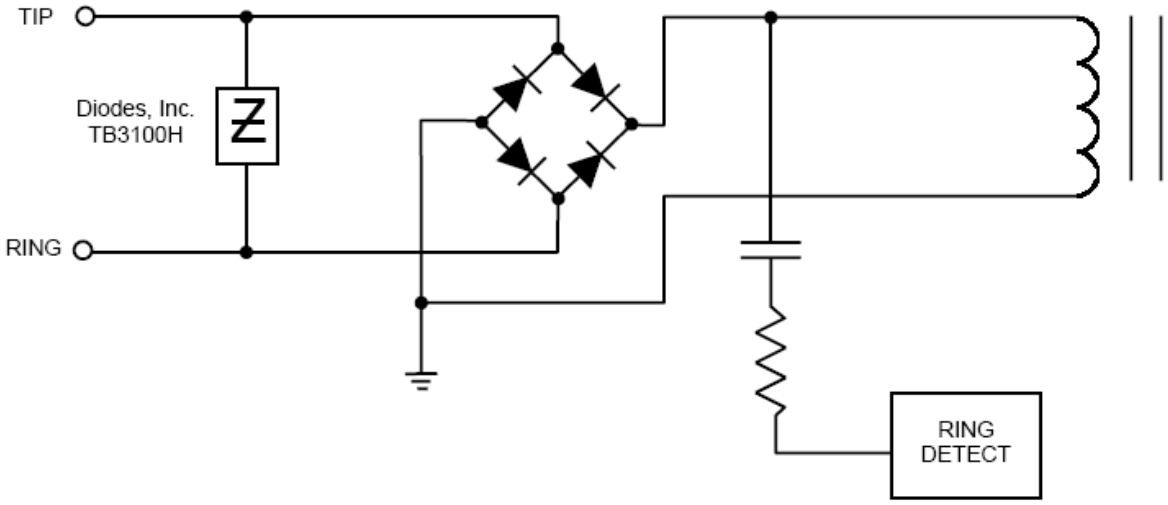


Figure 2. Typical Transformer Coupled Tip and Ring Interface

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