

OBSOLETE – PART DISCONTINUED

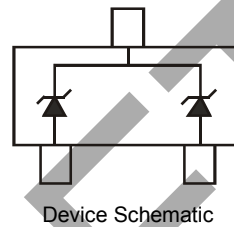
Features

- Dual TVS in Common Cathode Configuration for ESD Protection
- 40 Watt Peak Power Dissipation @1.0ms (Unidirectional)
- 225mW Power Dissipation
- Ideally Suited for Automated Insertion
- Low Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3 & 4)**



Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic.
UL Flammability Rating Classification 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208 [Ⓔ]
Lead-Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe)
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)

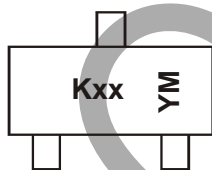


Ordering Information (Note 5 & 6)

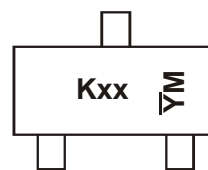
Part Number	Compliance	Case	Packaging
MMBZ15VDL-7-F	Standard	SOT23	3000/Tape & Reel
MMBZ15VDLQ-7-F	Automotive	SOT23	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Product manufactured with Date Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.
 5. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
 6. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



xx = Product Type Marking Code
 YM = Date Code Marking for Shanghai
 Assembly / Test site
 Y = Year (ex: 1 = 2021)
 M = Month (ex: 9 = September)



xx = Product Type Marking Code
 YM = Date Code Marking for Chengdu
 Assembly / Test site
 Y = Year (ex: 1 = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	T	U	G	H	I	J	K	L	M	N	O

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Power Dissipation (Note 7)	P_{PK}	40	W

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 8)	P_D	225	mW
Thermal Resistance, Junction to Ambient Air (Note 8)	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

$V_F = 0.9\text{V max @ } I_F = 10\text{mA}$

Type Number	Marking Code	V_{RWM}	Max Reverse Leakage I_R @ V_{RWM} (Note 9)	Breakdown Voltage				Max. Clamping Voltage $V_C @ I_{PP}$ (Note 7)		Typical Temperature Coefficient T_C (%/ $^\circ\text{C}$)
				V_{BR} (Note 9) (V)			@ I_T	V_C	I_{PP}	
				Min	Nom	Max	mA	V	A	
MMBZ15VDL	KVJ	12.8	100	14.3	15	15.8	1.0	21.2	1.9	+0.080

- Notes:
7. Non-repetitive current pulse per Figure 2 and derate above $T_A = +25^\circ\text{C}$ per Figure 1.
 8. Device mounted on FR-5 PCB $1.0 \times 0.75 \times 0.062$ inch pad layout as shown on Diodes Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>. 200mW per element must not be exceeded.
 9. Short duration pulse test used to minimize self-heating effect.

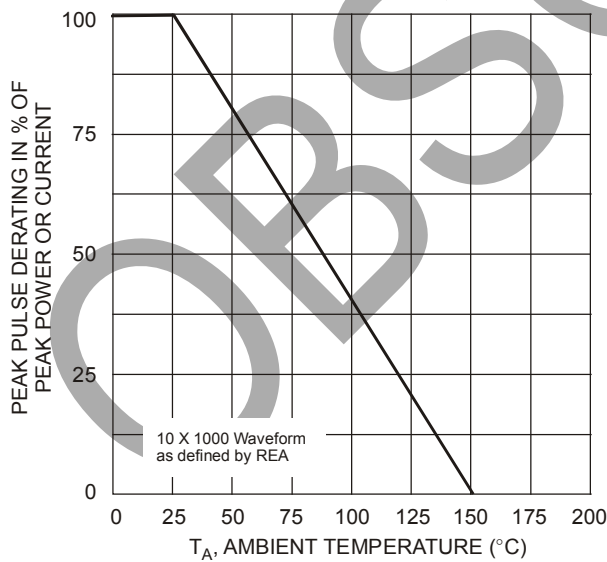


Figure 1 Pulse Derating Curve

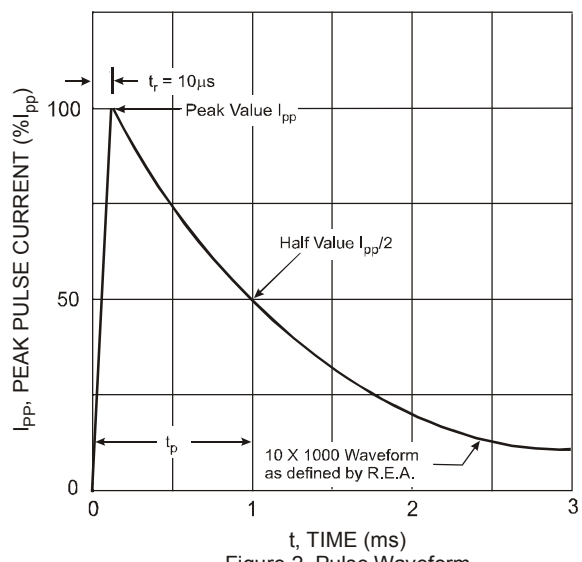


Figure 2 Pulse Waveform

OBSOLETE - PART DISCONTINUED

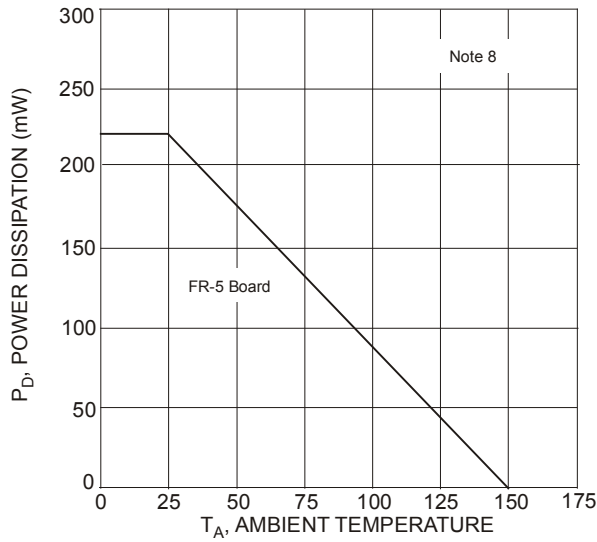


Figure 3 Steady State Power Derating Curve

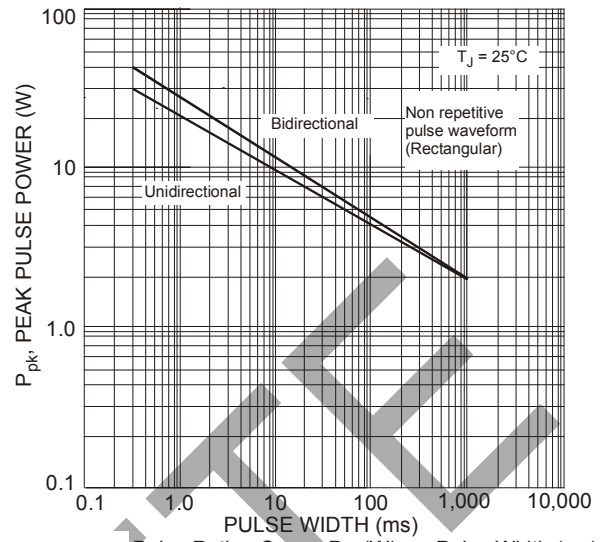


Figure 4 Pulse Rating Curve, P_{pk} (W) vs. Pulse Width (ms)

Power is defined as $P_{pk} = V_C \times I_{pp}$

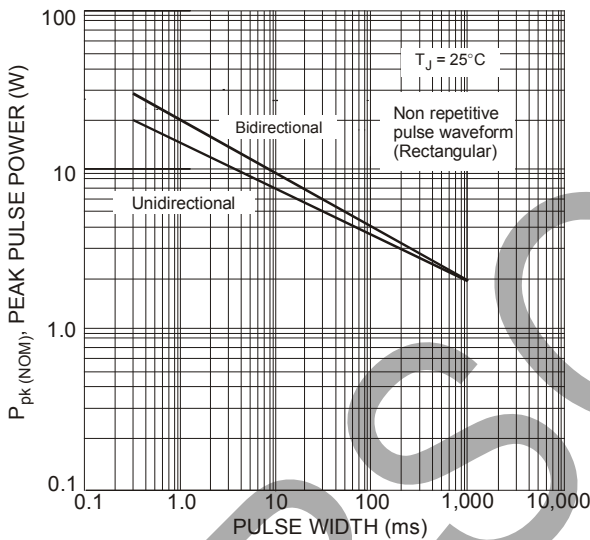
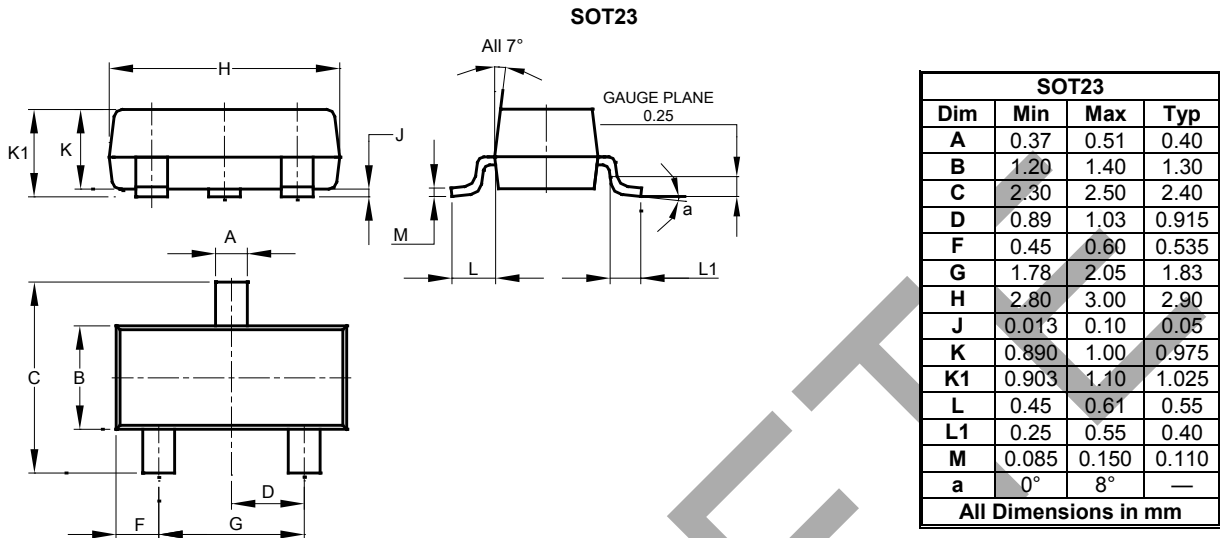


Figure 5 Pulse Rating Curve, P_{pk(NOM)} (W) vs. Pulse Width (ms)

Power is defined as $P_{pk(NOM)} = V_{BR(NOM)} \times I_{pp}$
where $V_{BR(NOM)}$ is the nominal breakdown voltage

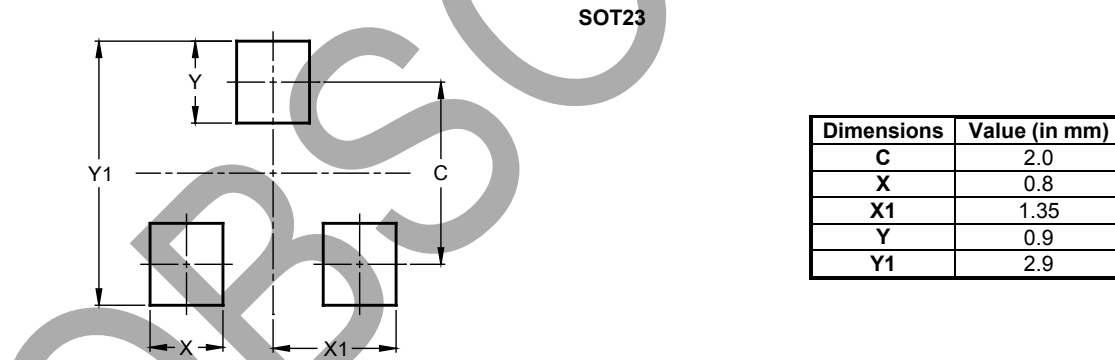
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



OBSOLETE - PART DISCONTINUED

IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (<https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/>) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com

OBSOLETE - PART DISCONTINUED