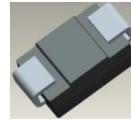
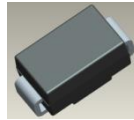


## Features

- Glass Passivated Die Construction
- Super-Fast Recovery Time for High Efficiency
- Surge Overload Rating to 40A Peak
- Ideally Suited for Automated Assembly
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

## Mechanical Data

- Package: SMB
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solder Plated Terminal - Solderable per MIL-STD-202, Method 208  
Lead Free Plating (Matte Tin Finish). Ⓔ③
- Polarity: Cathode Band or Cathode Notch
- Marking Information: As Marked on Body
- Weight: 0.093 grams (Approximate)



Top View

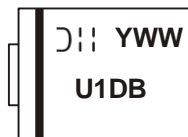
Bottom View

## Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
MURS120 -13-F	SMB	3000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



- U1DB = Product Type Marking Code
- ⌋⌋⌋ = Manufacturers' Code Marking
- YWW = Date Code Marking
- Y = Last Digit of Year (ex: 4 for 2024)
- WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	200	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage (Note 7) @ I <sub>R</sub> = 5μA	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	141	V
Average Rectified Output Current @ T <sub>T</sub> = +135°C	I <sub>O</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	40	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Total Capacitance (Note 6)	C <sub>T</sub>	27	pF
Typical Thermal Resistance, Junction to Terminal (Note 5)	R <sub>θJT</sub>	15	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Forward Voltage @ I <sub>F</sub> = 1.0A, T <sub>J</sub> = +25°C @ I <sub>F</sub> = 1.0A, T <sub>J</sub> = +150°C	V <sub>FM</sub>	0.875	V
		0.710	
Peak Reverse Current at Rated DC Blocking Voltage (Note 9) @ T <sub>A</sub> = +25°C @ T <sub>A</sub> = +150°C	I <sub>RM</sub>	2.0	μA
		50	
Reverse-Recovery Time (Note 7)	t <sub>RR</sub>	25	ns
Forward-Recovery Time (Note 8)	t <sub>FR</sub>	25	ns

- Notes:
5. Unit mounted on PC board with 5.0mm<sup>2</sup> (0.013mm thick) copper pads as heatsink.
  6. Measured at 1.0MHz and applied reverse voltage of 4V DC.
  7. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>RR</sub> = 0.25A. See Figure 5.
  8. Measured with I<sub>F</sub> = 1.0A, di/dt = 100A/μs, Duty Cycle ≤ 2.0%.
  9. Short duration pulse test used to minimize self-heating effect.

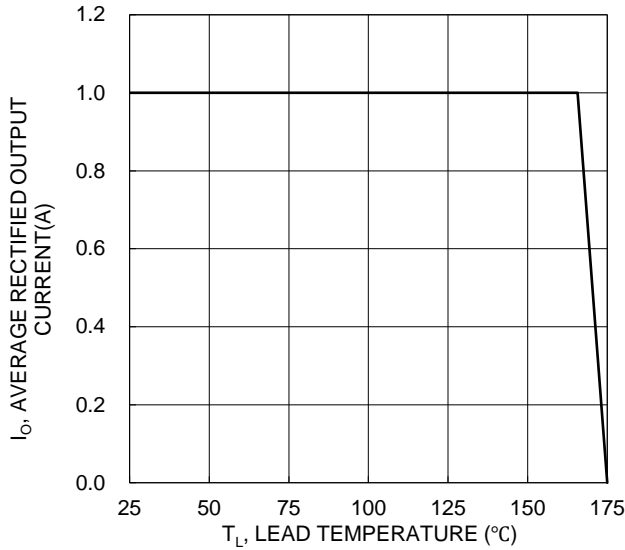


Fig. 1 Forward Current Derating Curve

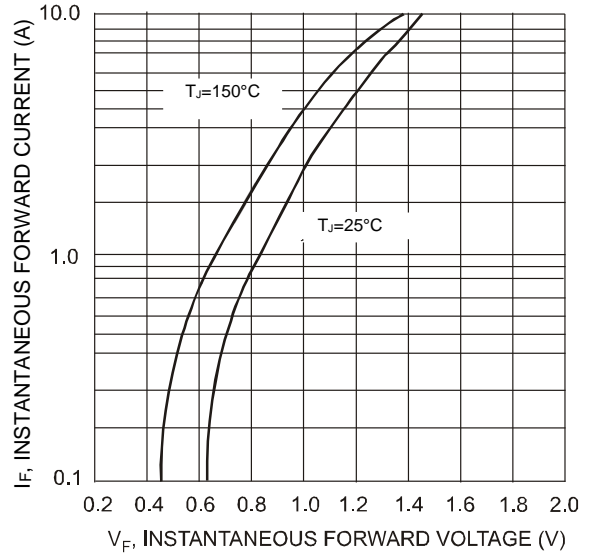


Fig. 2 Typical Forward Characteristics

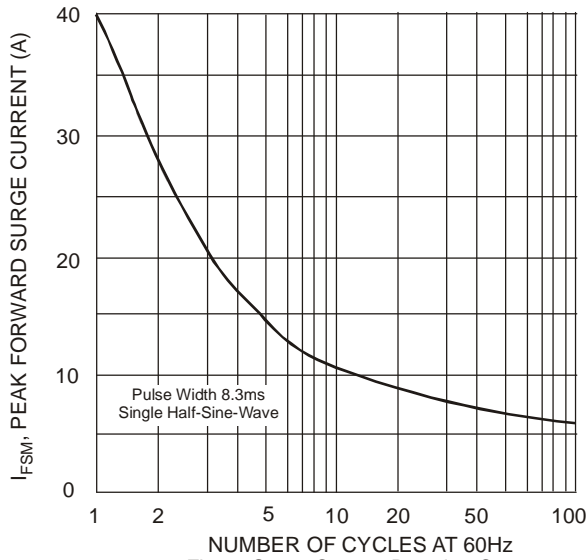


Fig. 3 Surge Current Derating Curve

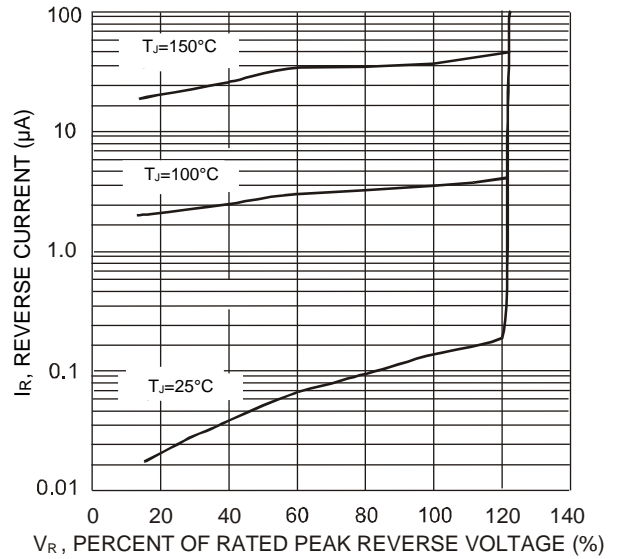
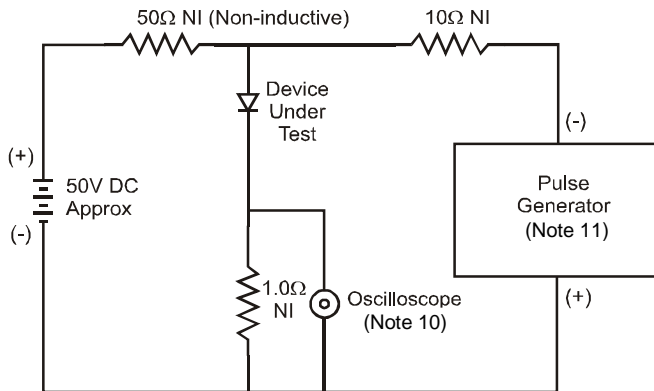
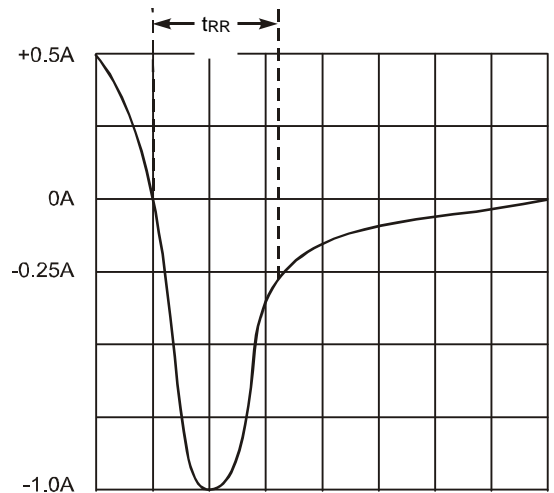


Fig. 4 Typical Reverse Characteristics



- Notes:  
 10. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.  
 11. Rise Time = 10ns max. Input Impedance = 50Ω.



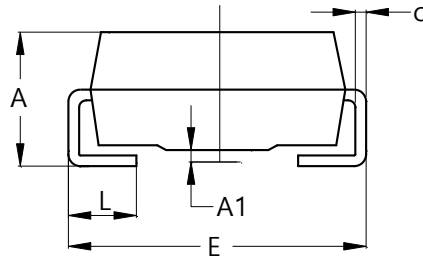
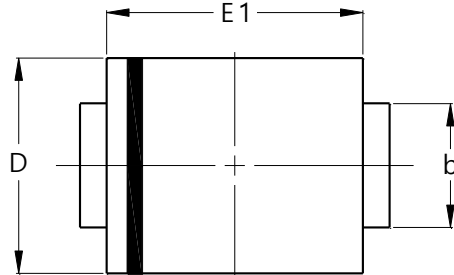
Set Time Base for 50/100 ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SMB**

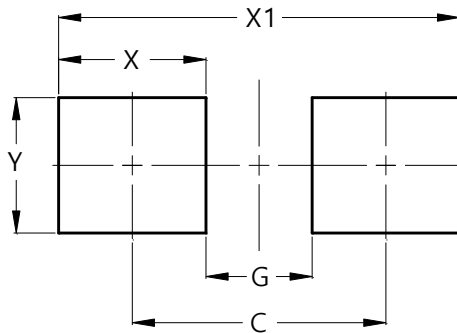


SMB		
Dim	Min	Max
A	2.00	2.50
A1	0.05	0.20
b	1.96	2.21
c	0.15	0.31
D	3.30	3.94
E	5.00	5.59
E1	4.06	4.57
L	0.76	1.52
All Dimensions in mm		

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SMB**



Dimensions	Value (in mm)
C	4.30
G	1.80
X	2.50
X1	6.80
Y	2.30

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