PART OBSOLETE - DISCONTINUED



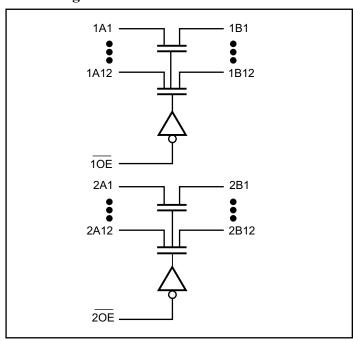
PI5C16211

24-Bit Bus Switch

Features

- Near-Zero propagation delay
- 5Ω switch connects inputs to outputs
- · Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
 - Ideally suited for notebook applications
- Industrial operating temperature: -40°C to +85°C
- Packaging (Pb-free & Green available):
 - 56-pin 240-mil wide thin plastic TSSOP

Block Diagram



Truth Table⁽¹⁾

₁ OE	₂ OE	₁ A, ₁ B, I/Os	2A, 2B, I/Os
L	L	$_{1}A = {_{1}B}$	$_2A = _2B$
L	Н	$_{1}A = {_{1}B}$	Z
Н	L	Z	$_2A = _2B$
Н	Н	Z	Z

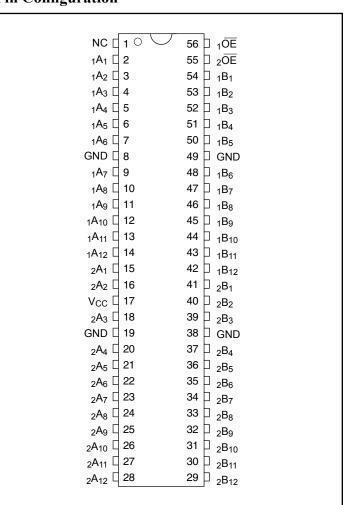
Notes:

H = High Voltage Level, L = Low Voltage Level,
 Z = High Impedance

Description

Pericom Semiconductor's PI5C16211 is a 24-bit bus switch designed with a low On-Resistance allowing inputs to be connected directly to outputs. This device operates as a 24-bit or a 12-bit bus switch, which provides high speed bus switching.

Pin Configuration



Pin Description

Pin Name	I/O	Description
xŌĒ	I	Output Enables
xAx	I/O	Bus A
xBx	I/O	Bus B



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C	to +150°C
Ambient Temperature with Power Applied –40°C	C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)0.5	V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)0.5	V to +7.0V
DC Input Voltage0.5	V to +7.0V
DC Output Current	120mA
Power Dissipation	1.4W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 5\text{V} \pm 10\%$)

Parameters	Description	Test Conditions		Typ ⁽¹⁾	Max.	Units	
V_{IK}	Clamp Diode Voltage	$V_{CC} = 4.5 \text{V}, I_{I} = -18 \text{mA}$			1.2	V	
Т.,	Input I OW Current	$V_{CC} = 5.5V$, $V_I = V_{CC}$ or GND			10		
I_{I}	Input LOW Current	$V_{CC} = 0V, V_{I} = 5.5V$			±1	μΑ	
I_{CC}	Quiescent Power Supply Current	$V_{CC} = 5.5V$, $V_I = V_{CC}$ or GND			3.0		
ΔI_{CC}	Supply Current per Control Input @ TTL High	V_{CC} = 5.5V, One input at 3.4V, other inputs at V_{CC} or GND			2.5	mA	
$C_{I}^{(2)}$	Control Input Capacitance	$V_I = 3V \text{ or } 0$			4.5	рF	
$C_{IO}(OFF)^{(2)}$	A/B Switch OFF Capacitance	$V_O = 3V$ or 0, OE = V_{CC}			5.5	РГ	
R _{ON}	Switch On Resistance ⁽³⁾	$V_{CC} = 4.0V, V_I = 2.4V, I_I = 15 \text{ mA}$		14	20		
		$V_{CC} = 4.5V, V_I = 0V, I_I = 64 \text{ mA}$		5	7	Ω	
		$V_{CC} = 4.5V, V_I = 0V, I_I = 30 \text{ mA}$		5	7] 12	
		$V_{CC} = 4.5V$, $I = 2.4V$, $I_I = 15$ mA		8	12		

Notes:

- 1. Typical values are at $V_{CC} = 5.0V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 2. This parameter is determined by device characterization but is not production tested.
- 3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

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Power Supply Characteristics

Parameters	Description	Min.	Typ. ⁽¹⁾	Max.	Units
V_{CC}	Supply Voltage	4.0		5.5	
$ m V_{IH}$	High Level Input Voltage	2.0			V
$V_{ m IL}$	Low Level Input voltage			0.8	
$T_{\mathbf{A}}$	Operating Free-Air Tempertaure	-40		85	°C

Note:

Switching Characteristics over Operating Range

Danamatana	From	То	Test	$V_{CC} = 5$	V ±0.5V	V _{CC}	= 4V	IIm:ta
Parameters	(Input)	(Output)	Conditions	Min.	Max.	Min.	Max.	Units
$t_{PD}^{(1)}$	A or B	B or A	a		0.25		0.25	
$t_{ m EN}$	ŌĒ	A or B	$C_L = 50 \text{pF}$ $R_L = 500 \Omega$	1.5	9.3		10.1	ns
$t_{ m DIS}$	OE	A or B	KL 50022	1.5	8.5		7.1	

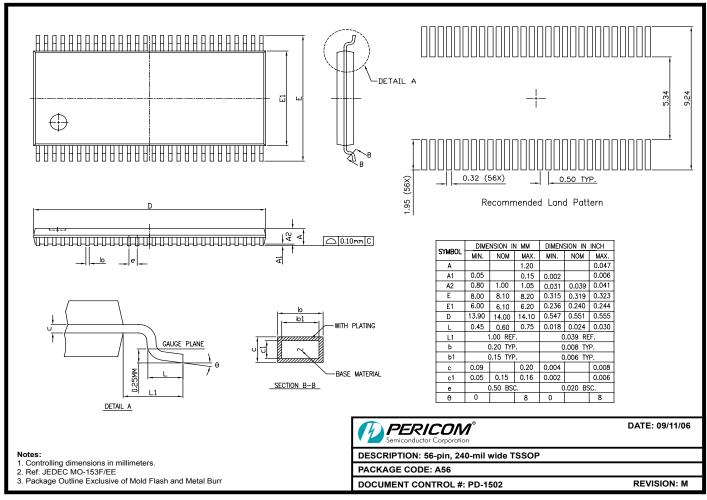
Notes:

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^{1.} Typical values are at $V_{CC} = 5.0V$, +25°C ambient.

^{1.} This parameter is guaranteed but not tested on Propagation Delays. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.





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Note:

• For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

Ordering Information

Ordering Code	Package Type	Package Description
PI5C16211AE	A	Pb-free & Green, 56-pin 240-mil wide TSSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

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