



# 10A SBR SUPER BARRIER RECTIFIER PowerDI5

#### Product Summary (@ TA = +25°C)

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F</sub> Max (V)	I <sub>R</sub> Max (mA)
45	10	0.53	0.4

### **Applications**

- DC/DC converters
- · AC/DC adaptors
- Bypass diodes

### **Features and Benefits**

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for +200°C maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier (SBR<sup>®</sup>) Technology
- Low-Forward Voltage Drop
- Excellent High-Temperature Stability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR10A45SP5Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

- Package: PowerDI<sup>®</sup>5
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208(§3)
- Weight: 0.093 grams (Approximate)

PowerDI5







Bottom View

LEFT PIN BOTTOM SIDE RIGHT PIN HEAT SINK

Note: Pins Left & Right must be electrically connected at the printed circuit board.

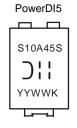
### **Ordering Information** (Note 4)

Orderable Part Number	Package	Packing		
Orderable Part Number	Package	Qty.	Carrier	
SBR10A45SP5-13	PowerDI5	5,000	Tape & Reel	
SBR10A45SP5-7	PowerDI5	1,500	Tape & Reel	
SBR10A45SP5Q-13	PowerDI5	5,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



S10A45S = Product Type Marking Code

| I = Manufacturer's Code Marking
| K = Factory Designator
| YYWW = Date Code Marking
| YY = Last Two Digits of Year (ex: 24 for 2024)
| WW = Week Code (01 to 53)

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Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	VRRM		
Working Peak Reverse Voltage	VRWM	45	V
DC Blocking Voltage	$V_{RM}$		
Average Rectified Output Current	lo	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	180	А

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit	
Typical Thermal Resistance Thermal Resistance Junction to Ambient (Note 5) Thermal Resistance Junction to Ambient (Note 6)		— RθJA RθJA		°C/W	
	V <sub>R</sub> ≤ 80% V <sub>RRM</sub>		-65 to +150		
Operating Temperature Range	V <sub>R</sub> ≤ 50% V <sub>RRM</sub>	TJ	≤180	°C	
	DC Forward Mode (Note 7)		≤200		
Storage Temperature Range		T <sub>STG</sub>	-65 to +175	°C	

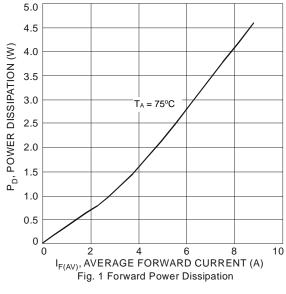
#### Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

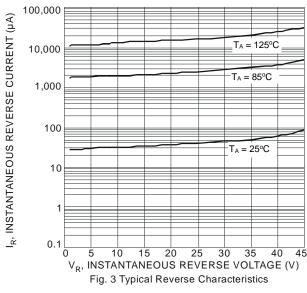
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V(BR)R	45	_		V	$I_R = 0.5 \text{mA}$
Forward Voltage Drop	VF		0.39 0.46	— 0.53	V	I <sub>F</sub> = 5A, T <sub>J</sub> = +25°C I <sub>F</sub> = 10A, T <sub>J</sub> = +25°C
Leakage Current (Note 8)	IR	_	_	0.4	mA	V <sub>R</sub> = 45V, T <sub>J</sub> = +25°C
Junction Capacitance	CJ	_	500	_	pF	$V_R = 4V, T_J = +25^{\circ}C$
Reverse Recovery Time	t <sub>RR</sub>	_	23	_	ns	IF = 0.5A, I <sub>R</sub> = 1A, I <sub>RR</sub> = 0.25A

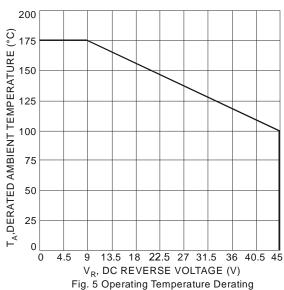
Notes:

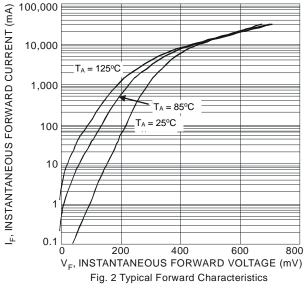
- FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
   Polymide PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
   Max junction temperature guaranteed for 2 hours.
   Short duration pulse test used to minimize self-heating effect.











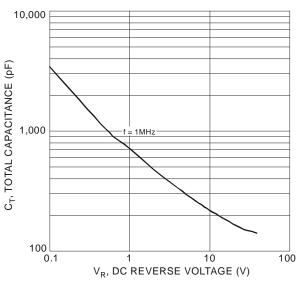


Fig. 4 Total Capacitance vs. Reverse Voltage

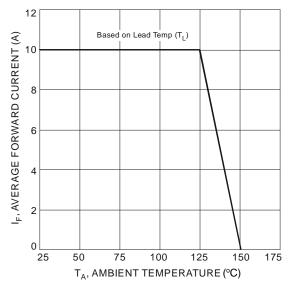


Fig. 6 Forward Current Derating Curve



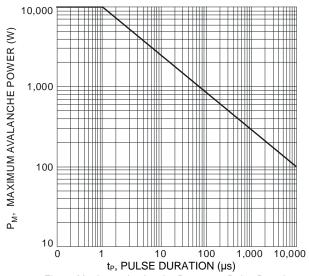


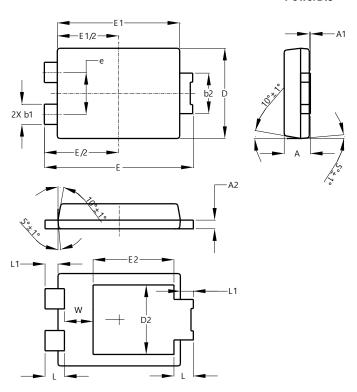
Fig. 7 Maximum Avalanche Power vs. Pulse Duration



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5

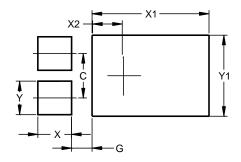


PowerDI5					
Dim	Min	Max	Тур		
Α	1.05	1.15	1.10		
A1	0.00	0.05			
A2	0.33	0.43	0.381		
b1	0.80	0.99	0.89		
b2	1.70	1.88	1.78		
D	3.90	4.05	3.966		
D2			3.054		
Е	6.40	6.60	6.51		
е			1.84		
E1	5.30	5.45	5.37		
E2			3.549		
L	0.75	0.95	0.85		
L1	0.50	0.65	0.57		
W	1.10	1.41	1.255		
All Dimensions in mm					

# **Suggested Pad Layout**

 $\label{lem:please} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 

#### PowerDI5



Dimensions	Value (in mm)		
С	1.840		
G	0.852		
Х	1.400		
X1	4.860		
X2	1.310		
Y	1.390		
V1	3 360		



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