



SBR3U40P1Q

3.0A SBR SUPER BARRIER RECTIFIER PowerDI123

Product Summary (@ TA = +25°C)

VRRM (V)	lo (A)	V _{F(Max)} (V)	I _{R(Max)} (μΑ)
40	3	0.47	400

Features and Benefits

- Ultra Low-Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier (SBR[®]) Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR3U40P1Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Applications

- DC-DC converters
- AC-DC rectifiers
- SMPS

Mechanical Data

- Package: PowerDI[®]123
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.018 grams (Approximate)

PowerDI123



Top View

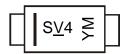
Ordering Information (Note 4)

Orderable Part Number	Paskaga	Packing		
Orderable Part Number	Package	Qty. Carrier		
SBR3U40P1Q-7	PowerDI123	3,000	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



SV4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: L = 2024) M = Month (ex: 9 = September)

Date Code Key

Year	2018	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	F	-	L	М	Ν	Р	R	S	Т	U	V	W
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vrm	40	V
RMS Reverse Voltage	VR(RMS)	28	V
Average Rectified Output Current	lo	3	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	IFSM	75	А
Non-Repetitive Energy at I _{AS} = 1.9A, L = 50mH	Eas	60	mJ

ESD Ratings

Characteristic	Symbol	Ratings	Unit
Human Body Mode ESD Protection	ESD HBM	4000	V
Machine Model ESD Protection	ESD MM	400	V
Charged Device Model	ESD CDM	1	kV

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance Junction to Soldering (Note 5) Thermal Resistance Junction to Ambient (Note 6) Thermal Resistance Junction to Ambient (Note 7) Thermal Resistance Junction to Ambient (Note 8)	Rejs Reja Reja Reja	5 145 125 75	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

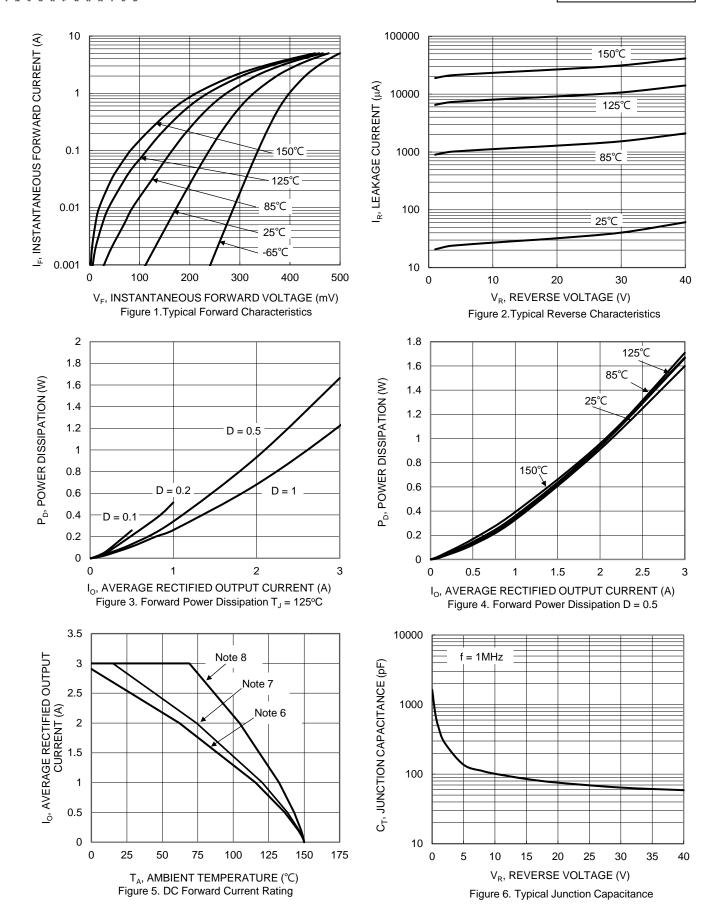
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	V _{(BR)R}	40	_	_	V	$I_R = 400 \mu A$
		_	0.30	0.34		I _F = 0.5A, T _J = +25°C
Forward Voltage Drop	V _F	_	0.34	0.39	-	$I_F = 1.0A, T_J = +25^{\circ}C$
		_	0.42	0.47		I _F = 3.0A, T _J = +25°C
Lockogo Current (Note O)	I _R	_	70	400	μΑ	V _R = 40V, T _J = +25°C
Leakage Current (Note 9)		_	8	40	mA	V _R = 40V, T _J = +125°C
Switching Speed	t _{RR}	_	12	_	ns	$I_F = 0.5A$, $I_R = 1A$, $I_{RR} = 0.25A$ (RG1)
Junction Capacitance	Сл	_	165	_	pF	V _R = 4V, T _J = +25°C

Notes:

- 5. Theoretical R_{BJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- FR-4 PCB, 2oz. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
 Polymide PCB, 2oz. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- 8. Part mounted on 1inch by 1inch FR-4 PCB, 2oz.
- 9. Short duration pulse test used to minimize self-heating effect.







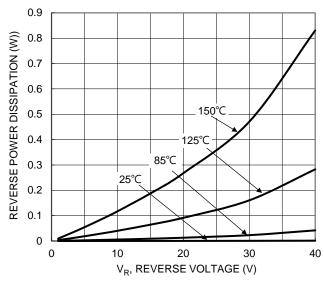


Figure 7. Typical Reverse Power Dissipation D = 0.5

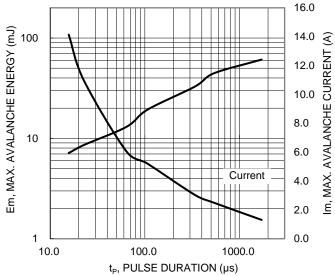


Figure 8. Typical Single Pulse Max. Avalanche Energy and Current

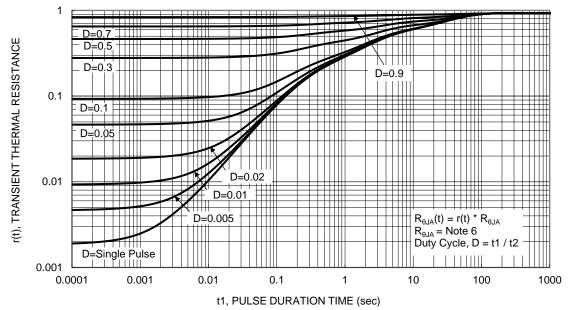


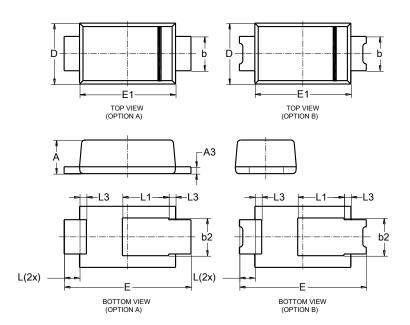
Figure 9. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

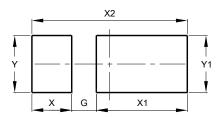


PowerDI123									
	FowerDI123								
Dim	Min	Max	Тур						
Α	0.93	1.00	0.98						
A3	0.15	0.25	0.20						
b	0.85	1.25	1.00						
b2	1.025	1.125	1.10						
D	1.63	1.93	1.78						
Е	3.50	3.90	3.70						
E1	2.60	3.00	2.80						
L	0.40	0.50	0.45						
L1	1.25	1.40	1.35						
L3	0.125	0.275	0.20						
All Dimensions in mm									

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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