



SCHOTTKY BARRIER RECTIFIER CHIP SCALE PACKAGE

Product Summary

BV _{RRM} (V)	lo (A)	VF Max (V)	I _R Max (μA) @10V		
40	1	0.62	100		

Description

The SDM1A40CSP-01 is a 40-volt 1A Schottky barrier rectifier that is optimized for low forward voltage drop and low leakage current, housed in a compact chip scale package (CSP) that occupies only 0.6mm² of board space. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space.

Applications

It is ideally suited for use in portable applications as a:

- Blocking Diode
- Boost Diode
- Switching Diode
- Reverse Protection Diode

Features and Benefits

- Off Board Profile of 0.275mm More than 30% Thinner than DFN1006
- Low Forward Voltage (VF) Minimizes Conduction Losses and Improves Efficiency
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High Temperature Operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: X3-WLB1006-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 @
- Polarity: Cathode Dot
- Weight: 0.001 grams (Approximate)



Ordering Information (Note 4)

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Part Number	Case	Packaging
SDM1A40CSP-01-7	X3-WLB1006-2	5,000/Reel

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

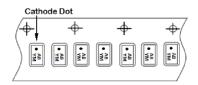
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



XA = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: D = 2016) M = Month (ex: 9 = September) Dot Denotes Cathode Pin



Date Code Key

Notes:

24.0 0040												
Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н		J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Average Rectified Output Current	lo	1	А
Repetitive peak Forward Current (Pulse Wave = 1msec, Duty Cycle = 25%)	IFRM	5	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	7	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	Reja	135	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	Reja	80	°C/W
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

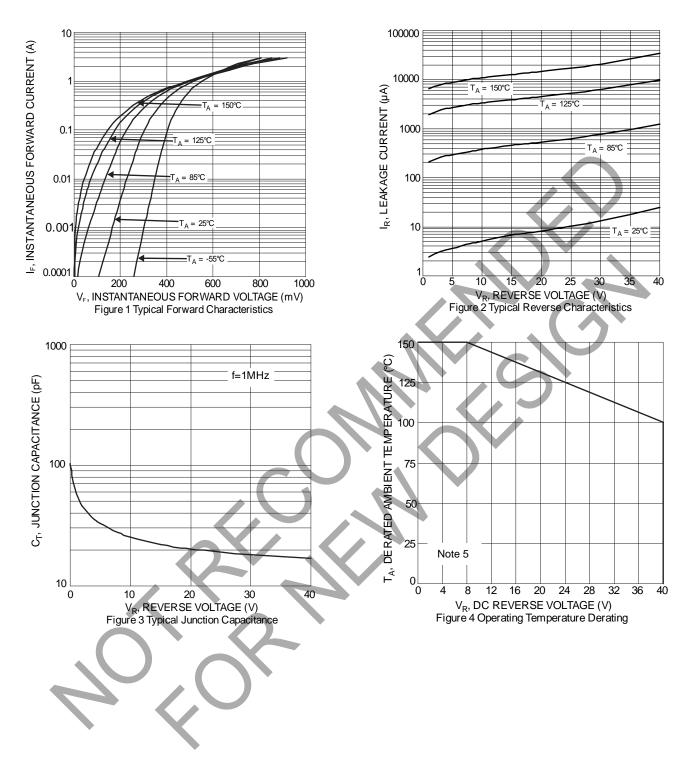
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF	-	0.51	0.62	V	I _F = 1.0A, T _J = +25°C
Leakage Current (Note 7)	IR		—	100	μA	V _R = 10V, T _J = +25°C
Junction Capacitance	Ст	-	35	—	pF	V _R = 4V, f = 1.0MHz

 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
6. Device mounted on FR-4 PCB, 2oz. 1 square inch Copper.
7. Short duration pulse test used to minimize self-heating effect. Notes:





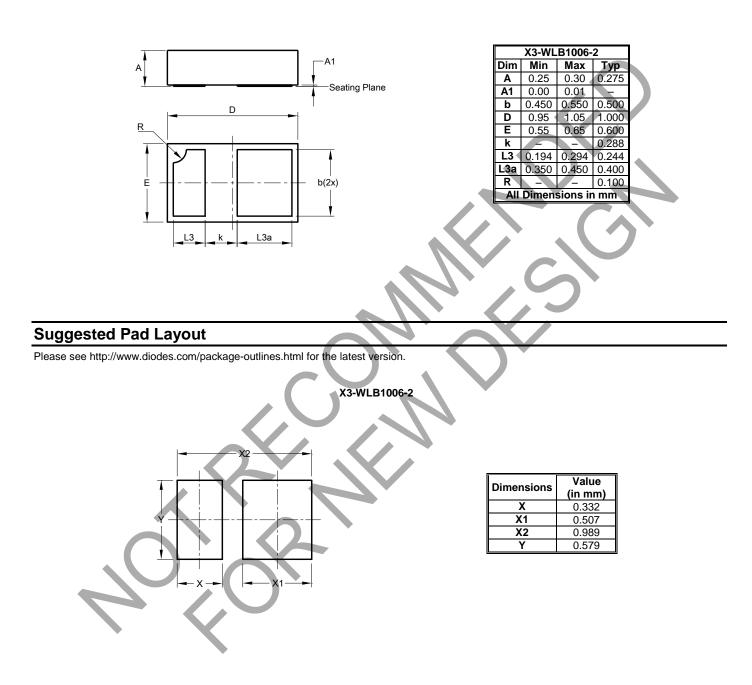




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







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