

0.5A TRENCH SCHOTTKY BARRIER RECTIFIER CHIP-SCALE PACKAGE

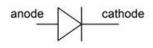
Product Summary

VRRM (V)	lo (A)	V _F Max (V)	I _R Max (μA)
30	0.5	0.55	9

Description and Applications

The SDT05U30CP3 is a 30-volt 0.5A trench Schottky barrier rectifier that is optimized for low-forward voltage drop and low-leakage current, housed in a compact chip-scale package (CSP) that occupies only 0.18mm² board space with low profile. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space. The SDT05U30CP3 are ideally suited for use in portable applications as:

- Blocking diodes
- Boost diodes
- Switching diodes
- · Reverse protection diodes



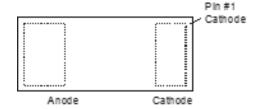
Device Schematic

Features and Benefits

- Low-forward voltage (V_F) minimizes conduction losses and improves efficiency.
- Low Leakage-Maximizes Battery Power
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation.
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: X3-DSN0603-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 (e4)
- Polarity: Cathode Dot
- Weight: 0.115mg (Approximate)



Ordering Information (Note 4)

Part Number	Packago	Packing	
	Package	Qty.	Carrier
SDT05U30CP3-7	X3-DSN0603-2	10,000	Tape & Reel

Notes:

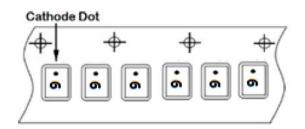
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Pin 1



6 = Product Type Marking Code Dot Denotes Cathode Pin





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
Average Rectified Output Current	lo	0.5	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	IFSM	7	А
ESD (Human Body Model) ESD (Machine Model)	ESD	8 0.4	kV

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R ₀ JA	220	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	Reja	80	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	Rejc	35	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	Rejc	20	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

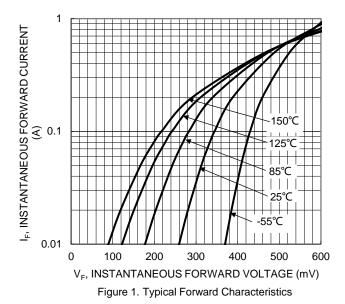
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF	_	0.26	0.33	V	IF = 10mA, TJ = +25°C
		_	0.35	0.41		IF = 100mA, T _J = +25°C
		_	0.39	0.47		IF = 200mA, T _J = +25°C
		_	0.49	0.55		IF = 500mA, T _J = +25°C
Reverse Current (Note 7)	IR	_	2.6	9	μA	V _R = 30V, T _J = +25°C
		_	1.1	_	mA	VR = 30V, TJ = +125°C
Junction Capacitance	Ст	_	18.8	_	pF	V _R = 10V, f = 1.0MHz

Notes:

- 5. Device mounted on FR-4 PCB, 2oz. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- Device mounted on 1inch square copper pad, 2oz.
 Short duration pulse test used to minimize self-heating effect.







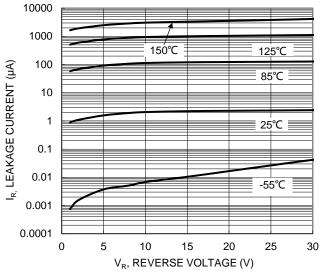


Figure 2. Typical Reverse Characteristics

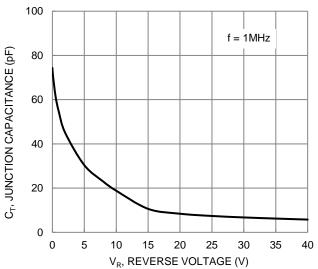


Figure 3. Typical Junction Capacitance

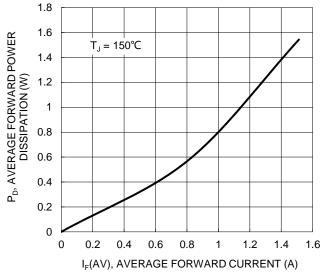


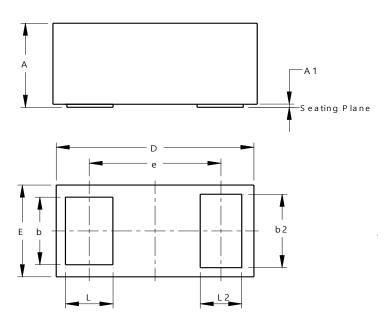
Figure 4. Forward Power Dissipation



Package Outline Dimensions (Note 8)

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN0603-2



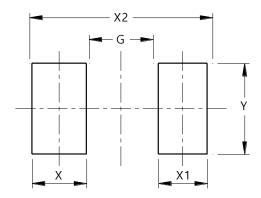
X3-DSN0603-2				
Dim	Min	Max	Тур	
Α	0.225	0.275	0.250	
A 1	0.00	0.02	1	
b	0.196	0.246	0.221	
b2	0.216	0.266	0.241	
D	0.575	0.625	0.600	
E	0.275	0.325	0.300	
е			0.400	
L	0.134	0.174	0.154	
L2	0.116	0.156	0.136	
All Dimensions in mm				

Note 8: Device side walls are electrically active bare silicon. Avoid contact of solder or flux on the side walls during the PCB assembly process.

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN0603-2



Dimensions	value	
פווטופווטווט	(in mm)	
G	0.262	
X	0.174	
X1	0.156	
X2	0.625	
Y	0.276	



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